

SHEET

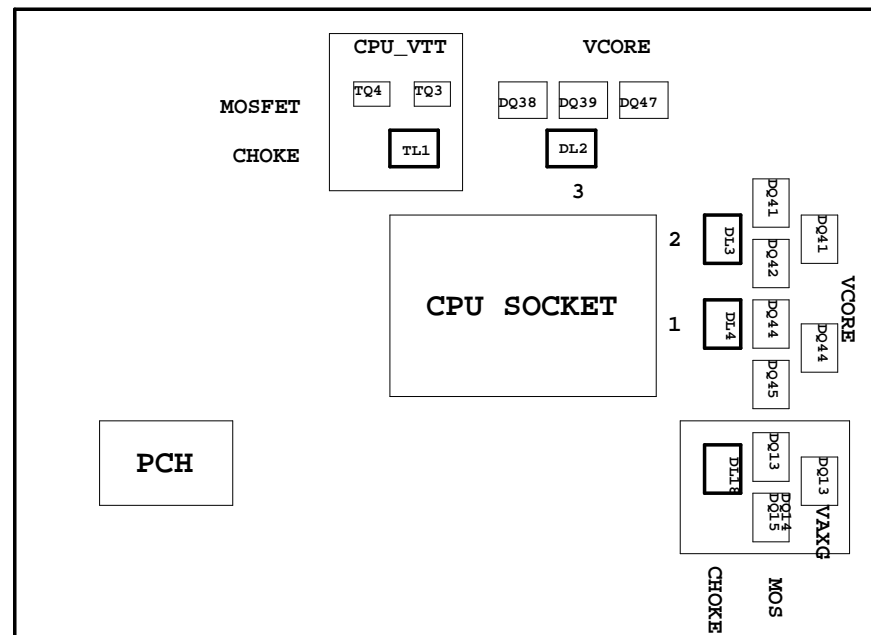
TITLE

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SHEET

TITLE

28	F_PANEL , F_USB
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30	HWM,KB/MS , FAN CTRL
31	ARTHEROS AR8161
32	mSATA
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34	DVI
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Gigabyte Technology

Title		
Cover Sheet		
Size	Document Number	Rev
Custom	GA-B75-D3V	1.1
Date:	Wednesday, July 04, 2012	Sheet 1 of 34

### Component value change history

## Circuit or PCB layout change

**Gigabyte Technology**

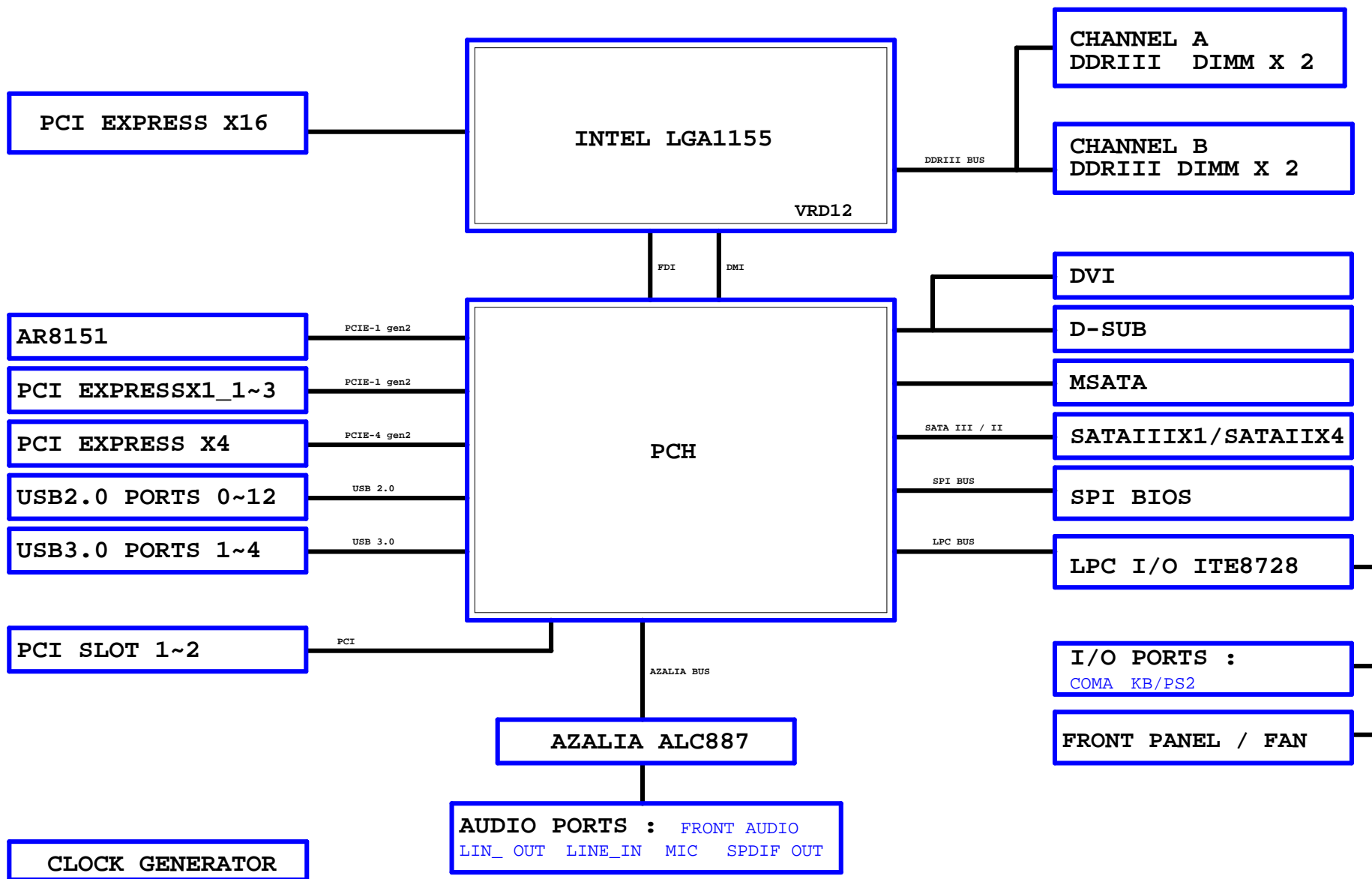
Title	BOM & PCB MODIFY HISTORY
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Size Custom	Document Number <b>GA-B75-D3V</b>
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Rev	
4.4	

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# BLOCK DIAGRAM





## LGA1155A

MAAA0	AV27	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[4]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[5]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[6]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	SA_DQ[4]	AJ2	MDA4
MAAA9	AT22	SA_MA[9]	SA_DQ[5]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			

7	-SWEA	-SWEA	AW29	SA_WE#	
7	-SCASA	-SCASA	AV30	SA_CAS#	
7	-SRASA	-SRASA	AU28	SA_RAS#	

7	SBA00	SBA00	AY29	SA_BS[0]	
7	SBA01	SBA01	AW28	SA_BS[1]	
7	SBA02	SBA02	AV20	SA_BS[2]	

7	-CSA0	-CSA0	AU29	SA_CS#0	
7	-CSA1	-CSA1	AV32	SA_CS#1	
7	-CSA2	-CSA2	AW30	SA_CS#2	
7	-CSA3	-CSA3	AU33	SA_CS#3	

7	CKEA0	CKEA0	AV19	SA_CKE[0]	
7	CKEA1	CKEA1	AT19	SA_CKE[1]	
7	CKEA2	CKEA2	AU18	SA_CKE[2]	
7	CKEA3	CKEA3	AV18	SA_CKE[3]	

	MODT_A0	AV31	SA_ODT[0]		
	MODT_A1	AU32	SA_ODT[1]		
	MODT_A2	AU30	SA_ODT[2]		
	MODT_A3	AW33	SA_ODT[3]		

7	DCLKA0	DCLKA0	AY25	SA_CK[0]	
7	-DCLKA0	-DCLKA0	AW25	SA_CK#0	
7	DCLKA1	DCLKA1	AU24	SA_CK[1]	
7	-DCLKA1	-DCLKA1	AU25	SA_CK#1	
7	DCLKA2	DCLKA2	AW27	SA_CK[2]	
7	-DCLKA2	-DCLKA2	AY27	SA_CK#2	
7	DCLKA3	DCLKA3	AV26	SA_CK[3]	
7	-DCLKA3	-DCLKA3	AW26	SA_CK#3	

7,8 -DDR3\_RST TR1 04/SHT/M/X

TBC9  
0.1u4/X7R/16V/K/X

SM\_DRAMRST#

AV13	SA_DQS[8]	
AV12	SA_DQS#8	
AU12	SA_ECC_CB[0]	
AU14	SA_ECC_CB[1]	
AW13	SA_ECC_CB[2]	
AY13	SA_ECC_CB[3]	
AU13	SA_ECC_CB[4]	
AU11	SA_ECC_CB[5]	
AY12	SA_ECC_CB[6]	
AW12	SA_ECC_CB[7]	

DDR\_0

1 OF 10

CPU-SK/1155/S/15

AN1	MDA8	
AN4	MDA9	
AR3	MDA10	
AR4	MDA12	
AN2	MDA11	
AN3	MDA13	
AR2	MDA14	
AR1	MDA15	

AW4	DQSA2	
AW4	-DQSA2	

AV2	MDA16	
AW3	MDA17	
AV5	MDA18	
AW5	MDA19	
AU2	MDA20	
AU3	MDA21	
AU5	MDA22	
AY5	MDA23	

AV8	DQSA3	
AW8	-DQSA3	

AY7	MDA24	
AU7	MDA25	
AV9	MDA26	
AU9	MDA27	
AV7	MDA28	
AW7	MDA29	
AW9	MDA30	
AY9	MDA31	

AV37	DQSA4	
AV36	-DQSA4	

AU35	MDA32	
AW37	MDA33	
SA_DQ[32]	MDA34	
SA_DQ[33]	MDA35	
SA_DQ[34]	MDA36	
SA_DQ[35]	MDA37	
SA_DQ[36]	MDA38	
SA_DQ[37]	MDA39	
SA_DQ[38]	MDA40	

AP38	DQSA5	
AP39	-DQSA5	

AR40	MDA40	
AR37	MDA41	
AN38	MDA42	
AN37	MDA43	
AR39	MDA44	
AR38	MDA45	
AN39	MDA46	
AN40	MDA47	

AK38	DQSA6	
AK39	-DQSA6	

AL40	MDA48	
AL37	MDA49	
AJ38	MDA50	
AJ37	MDA51	
AL39	MDA52	
AL38	MDA53	
AJ39	MDA54	
AJ40	MDA55	

AF38	DQSA7	
AF39	-DQSA7	

AG40	MDA56	
AG37	MDA57	
AE38	MDA58	
AE37	MDA59	
AG39	MDA60	
AG38	MDA61	
AE39	MDA62	
AE40	MDA63	

7 MODT\_A[0..3] MODT\_A[0..3]

8 MODT\_B[0..3] MODT\_B[0..3]

7 MDA[0..63] MDA[0..63]

8 MDB[0..63] MDB[0..63]

7 DQSA[0..7] DQSA[0..7]

7 -DQSA[0..7] -DQSA[0..7]

7 MAAA[0..15] MAAA[0..15]

8 MAAB[0..15] MAAB[0..15]

8 DQSB[0..7] DQSB[0..7]

8 -DQSB[0..7] -DQSB[0..7]

## LGA1155B

MAAB0	AK24	SB_MA[0]	SB_DQS[0]	AH7	DQSB0
MAAB1	AM20	SB_MA[1]	SB_DQS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AY17	SB_MA[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]			
MAAB13	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MA[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AV16	SB_MA[15]			

8	-SWEB	-SWEB	AR25	SB_WE#	
8	-SCASB	-SCASB	AK25	SB_CAS#	
8	-SRASB	-SRASB	AP24	SB_RAS#	

8	SBAB0	SBAB0	AP23	SB_BS[0]	
8	SBAB1	SBAB1	AM24	SB_BS[1]	
8	SBAB2	SBAB2	AW17	SB_BS[2]	

8	-CSB0	-CSB0	AN25	SB_CS#0	
8	-CSB1	-CSB1	AN26	SB_CS#1	
8	-CSB2	-CSB2	AL25	SB_CS#2	
8	-CSB3	-CSB3	AT26	SB_CS#3	

8	CKEB0	CKEB0	AU16	SB_CKE[0]	
8	CKEB1	CKEB1	AY15	SB_CKE[1]	
8	CKEB2	CKEB2	AW15	SB_CKE[2]	
8	CKEB3	CKEB3	AV15	SB_CKE[3]	

	MODT_B0	AL26	SB_ODT[0]		
	MODT_B1	AP26	SB_ODT[1]		
	MODT_B2	AM26	SB_ODT[2]		
	MODT_B3	AK26	SB_ODT[3]		

8	DCLKB0	DCLKB0	AL21	SB_CK[0]	
8	-DCLKB0	-DCLKB0	AL22	SB_CK#0	
8	DCLKB1	DCLKB1	AK20	SB_CK[1]	
8	-DCLKB1	-DCLKB1	AK21	SB_CK#1	
8	DCLKB2	DCLKB2	AL23	SB_CK[2]	
8	-DCLKB2	-DCLKB2	AM22	SB_CK#2	
8	DCLKB3	DCLKB3	AP21	SB_CK[3]	
8	-DCLKB3	-DCLKB3	AN21	SB_CK#3	

8	VREF_DQB	AH1	FC_AH1		
7	VREF_DQA	AH4	FC_AH4		

AN16	SB_DQS[8]	
AN15	SB_DQS#8	
AL16	SB_ECC_CB[0]	
AL16	SB_ECC_CB[1]	
AR16	SB_ECC_CB[2]	
AL15	SB_ECC_CB[3]	
AR15	SB_ECC_CB[4]	
AP15	SB_ECC_CB[5]	
AP15	SB_ECC_CB[7]	

AN29	DQSB4	
AN28	-DQSB4	

AR28	MDB32	
AR23	MDB33	
AL28	MDB34	
AL29	MDB35	
AP28	MDB36	
AP29	MDB37	
AM28	MDB38	
AM29	MDB39	

AP33	DQSB5	
AR33	-DQSB5	

AP32	MDB40	
AP21	MDB41	
AP35	MDB42	
AP34	MDB43	
AR32	MDB44	
AR31	MDB45	
AR35	MDB46	
AR34	MDB47	

AL33	DQSB6	
AM33	-DQSB6	

AM32	MDB48	
AM31	MDB49	
AL35	MDB50	
AL32	MDB51	
AM34	MDB52	
AL31	MDB53	
AM35	MDB54	
AL34	MDB55	

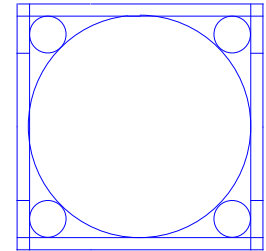
AG35	DQSB7	
AG34	-DQSB7	

AH35	MDB56	
AH34	MDB57	
AE34	MDB58	
AE35	MDB59	
AJ35	MDB60	
AJ34	MDB61	
AF33	MDB62	
AF35	MDB63	

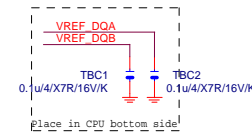
DDR\_1

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CPU-SK/1155/S/15

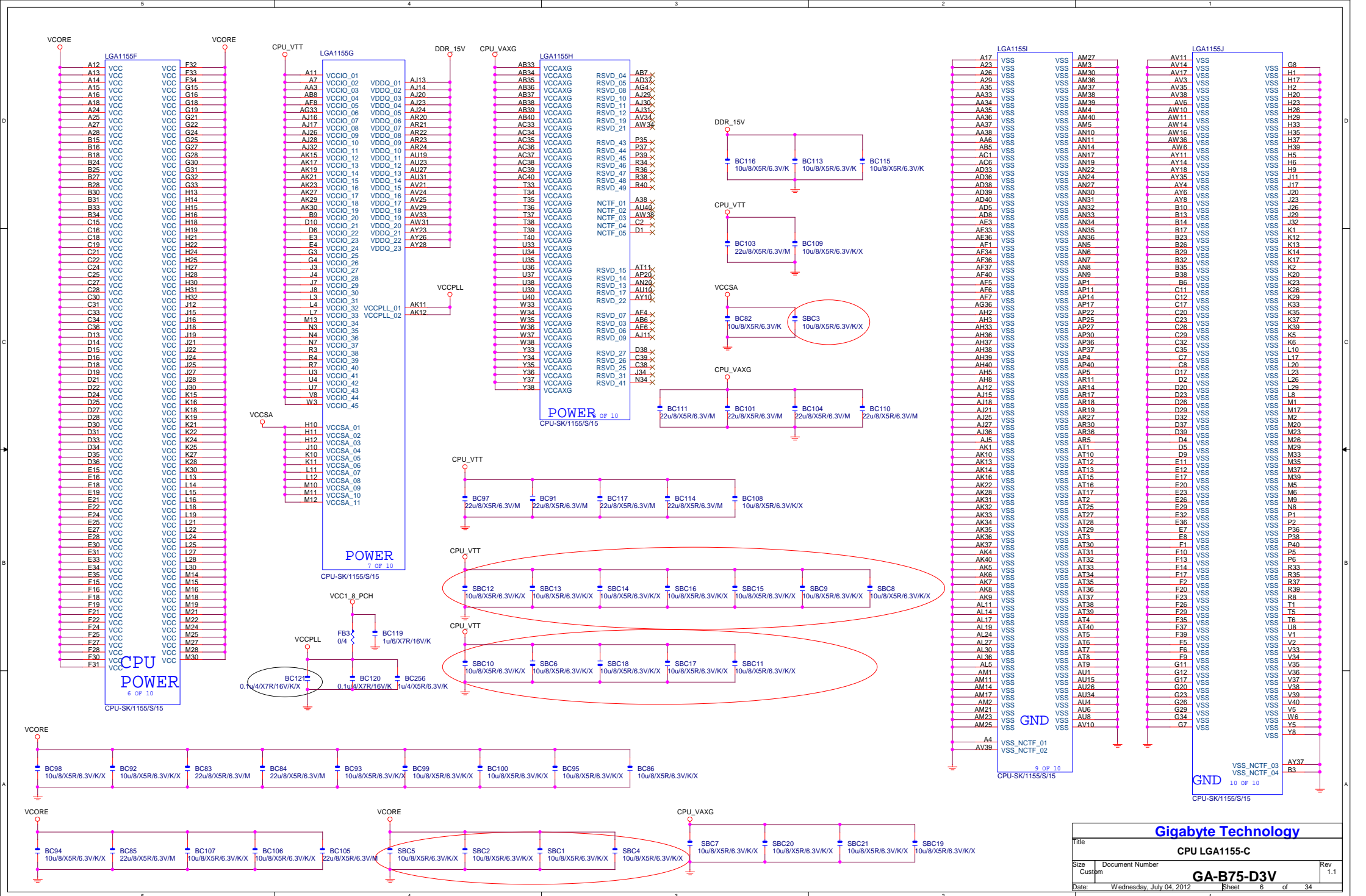
LGA1155  
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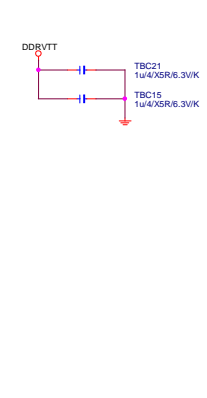
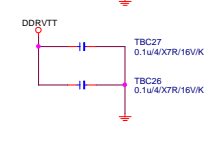
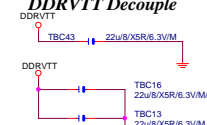
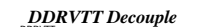
Need check the new CPU ME



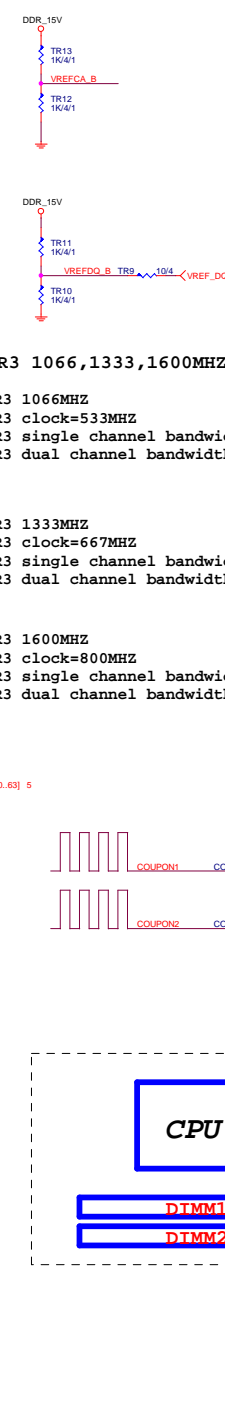
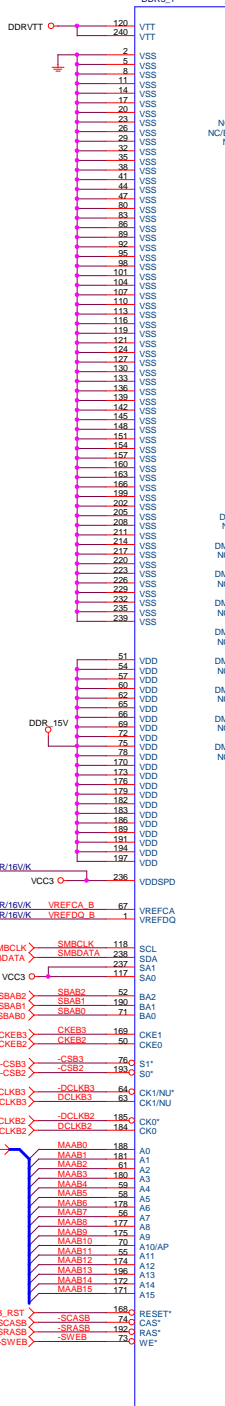
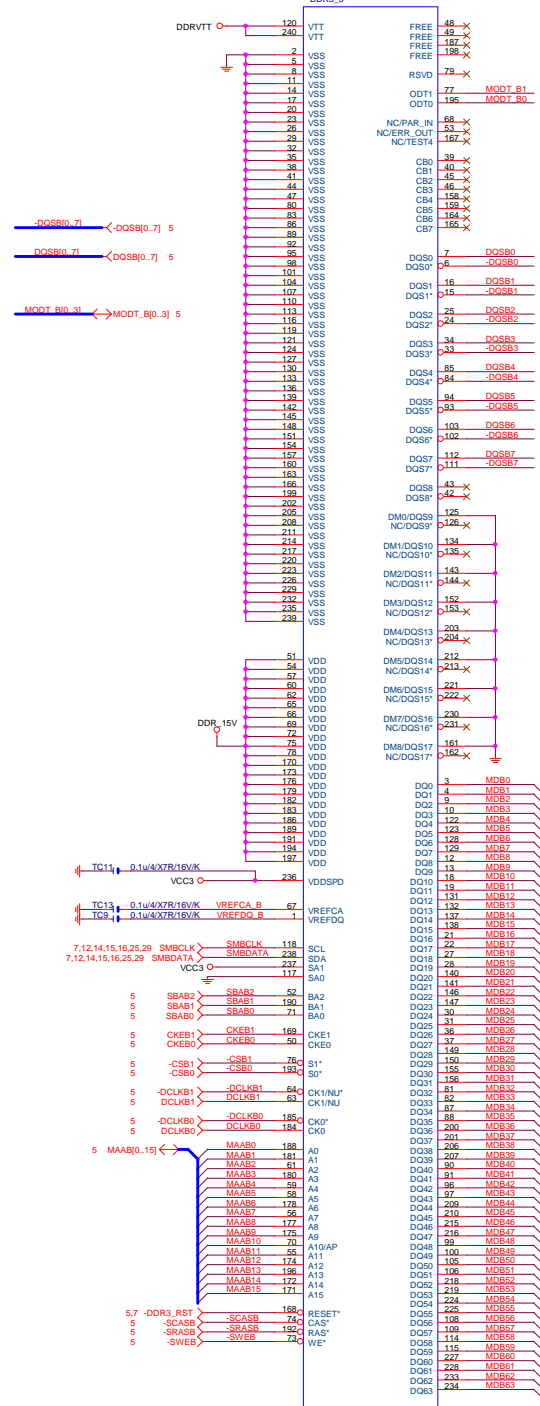
Gigabyte Technology

Title			CPU LGA1155-B		
Size	Document Number			Rev	1.1
Custom					
			GA-B75-D3V		
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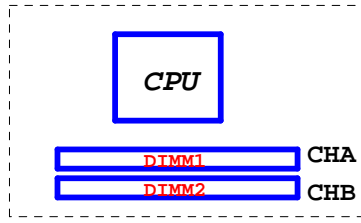
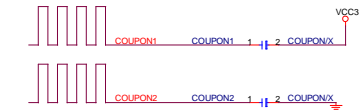


DDR3 1066,1333,1600MHZ BANDWIDTH

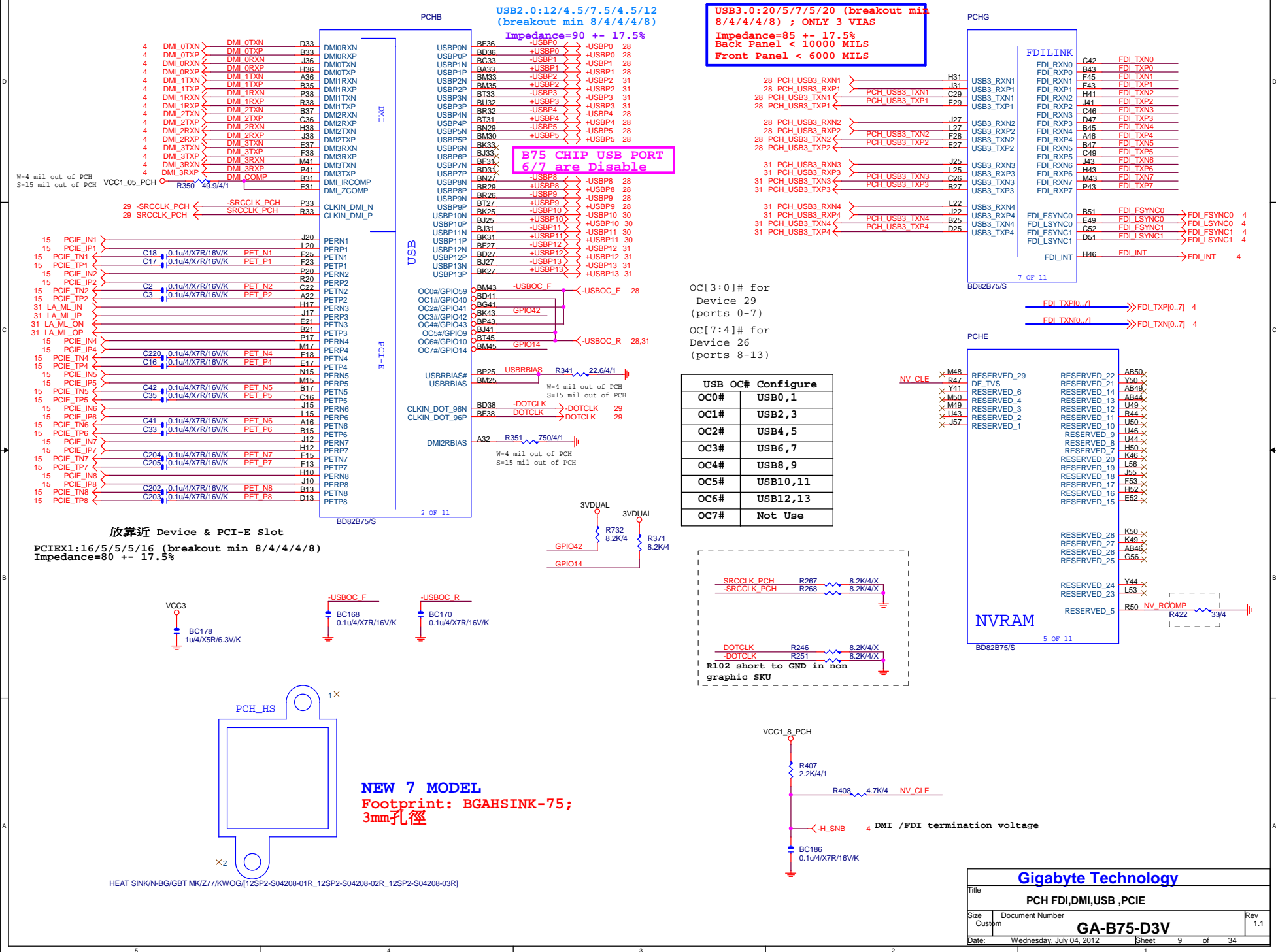
DDR3 1066MHZ  
DDR3 clock=533MHZ  
DDR3 single channel bandwidth=533x2x8Byte=8.5GB/s  
DDR3 dual channel bandwidth=533x2x2x8Byte=17GB/s

DDR3 1333MHZ  
DDR3 clock=667MHZ  
DDR3 single channel bandwidth=10.6GB/s  
DDR3 dual channel bandwidth=21GB/s

DDR3 1600MHZ  
DDR3 clock=800MHZ  
DDR3 single channel bandwidth=12.8GB/s  
DDR3 dual channel bandwidth=25.6GB/s



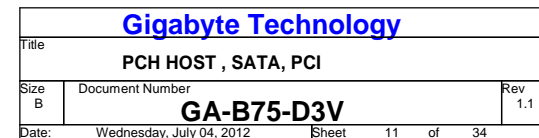
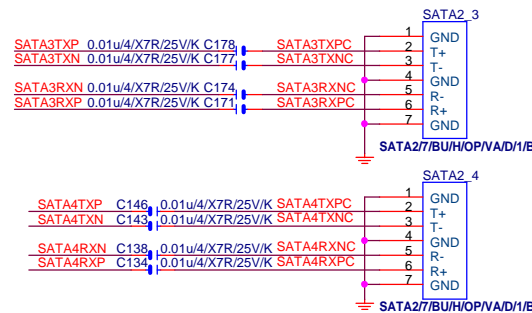
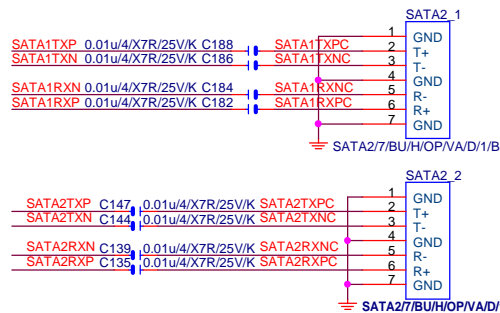
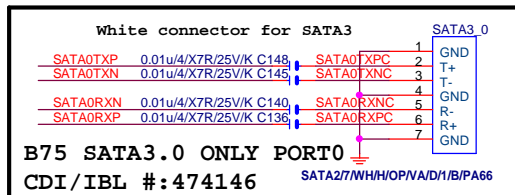
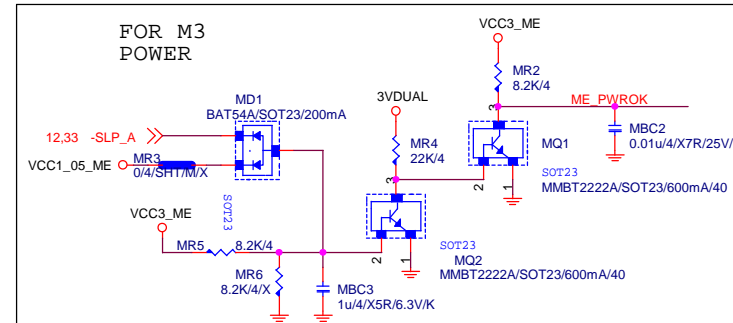
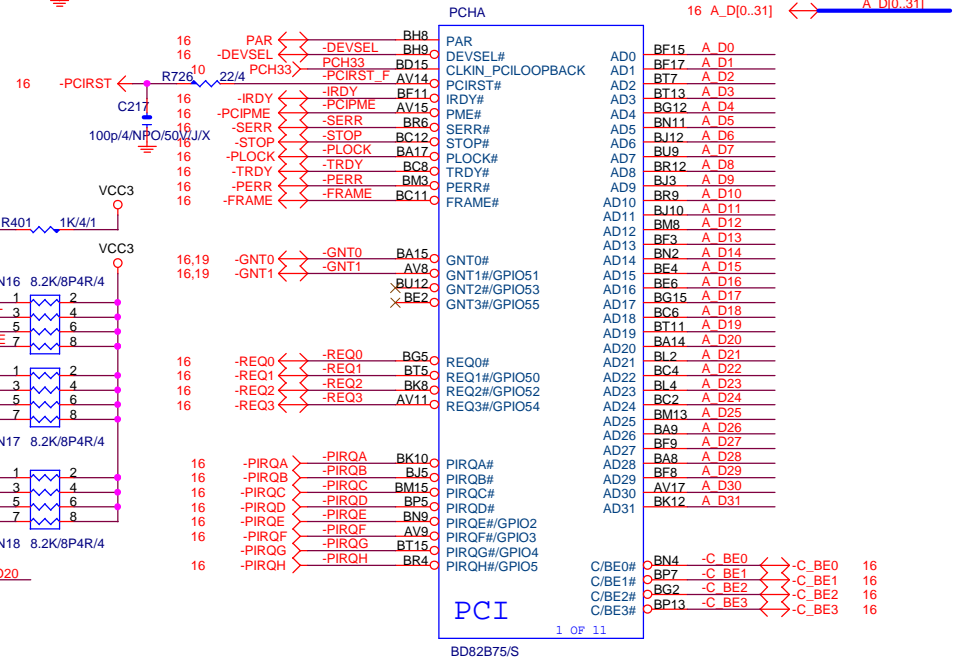


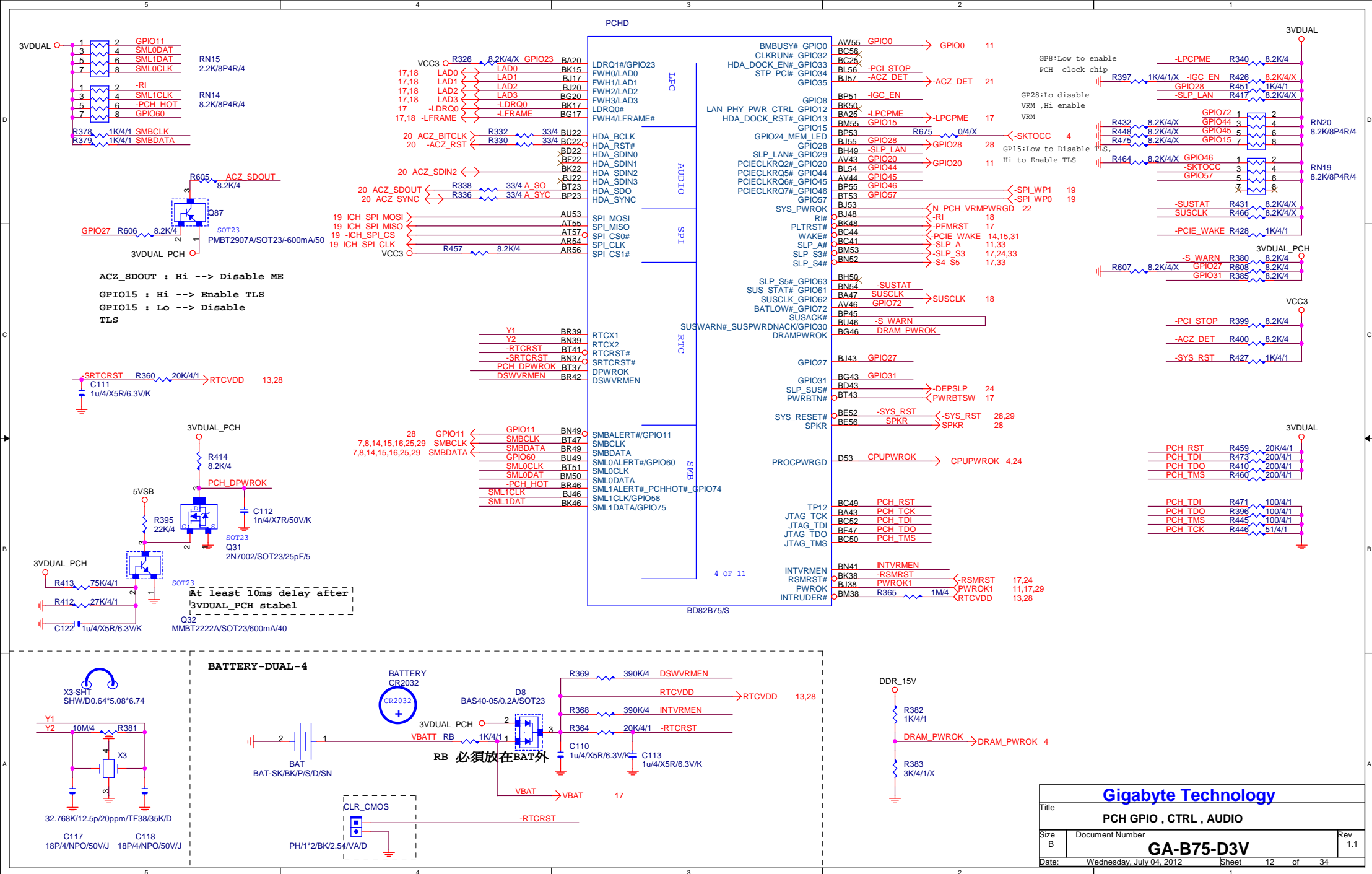




SRCCLK\_SATA R265 8.2K/4/

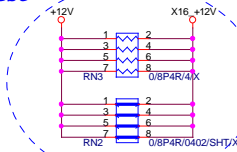
-SRCCLK\_SATA R266 8.2K/4/







+12 protect  
short-wire  
test



PCIE16:16/5/5/5/16

EXP\_RXP[0..15] >> EXP\_RXP[0..15] 4  
EXP\_RXN[0..15] >> EXP\_RXN[0..15] 4  
EXP\_TXP[0..15] >> EXP\_TXP[0..15] 4  
EXP\_TXN[0..15] >> EXP\_TXN[0..15] 4

EXP_TXP0	C43	0.22u/4X5R6.3V/KEXP_TXP0C
EXP_TXN0	C36	0.22u/4X5R6.3V/KEXP_TXN0C
EXP_TXP1	C47	0.22u/4X5R6.3V/KEXP_TXP1C
EXP_TXN1	C49	0.22u/4X5R6.3V/KEXP_TXN1C
EXP_TXP2	C52	0.22u/4X5R6.3V/KEXP_TXP2C
EXP_TXN2	C54	0.22u/4X5R6.3V/KEXP_TXN2C
EXP_TXP3	C57	0.22u/4X5R6.3V/KEXP_TXP3C
EXP_TXN3	C59	0.22u/4X5R6.3V/KEXP_TXN3C
EXP_TXP4	C62	0.22u/4X5R6.3V/KEXP_TXP4C
EXP_TXN4	C64	0.22u/4X5R6.3V/KEXP_TXN4C
EXP_TXP5	C65	0.22u/4X5R6.3V/KEXP_TXP5C
EXP_TXN5	C67	0.22u/4X5R6.3V/KEXP_TXN5C
EXP_TXP6	C69	0.22u/4X5R6.3V/KEXP_TXP6C
EXP_TXN6	C71	0.22u/4X5R6.3V/KEXP_TXN6C
EXP_TXP7	C76	0.22u/4X5R6.3V/KEXP_TXP7C
EXP_TXN7	C75	0.22u/4X5R6.3V/KEXP_TXN7C
EXP_TXP8	C79	0.22u/4X5R6.3V/KEXP_TXP8C
EXP_TXN8	C80	0.22u/4X5R6.3V/KEXP_TXN8C
EXP_TXP9	C81	0.22u/4X5R6.3V/KEXP_TXP9C
EXP_TXN9	C82	0.22u/4X5R6.3V/KEXP_TXN9C
EXP_TXP10	C86	0.22u/4X5R6.3V/KEXP_TXP10C
EXP_TXN10	C87	0.22u/4X5R6.3V/KEXP_TXN10C
EXP_TXP11	C90	0.22u/4X5R6.3V/KEXP_TXP11C
EXP_TXN11	C91	0.22u/4X5R6.3V/KEXP_TXN11C
EXP_TXP12	C92	0.22u/4X5R6.3V/KEXP_TXP12C
EXP_TXN12	C93	0.22u/4X5R6.3V/KEXP_TXN12C
EXP_TXP13	C95	0.22u/4X5R6.3V/KEXP_TXP13C
EXP_TXN13	C96	0.22u/4X5R6.3V/KEXP_TXN13C
EXP_TXP14	C97	0.22u/4X5R6.3V/KEXP_TXP14C
EXP_TXN14	C98	0.22u/4X5R6.3V/KEXP_TXN14C
EXP_TXP15	C99	0.22u/4X5R6.3V/KEXP_TXP15C
EXP_TXN15	C100	0.22u/4X5R6.3V/KEXP_TXN15C

PCI-E REV:1.1--> 2.5GHZ

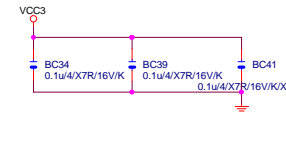
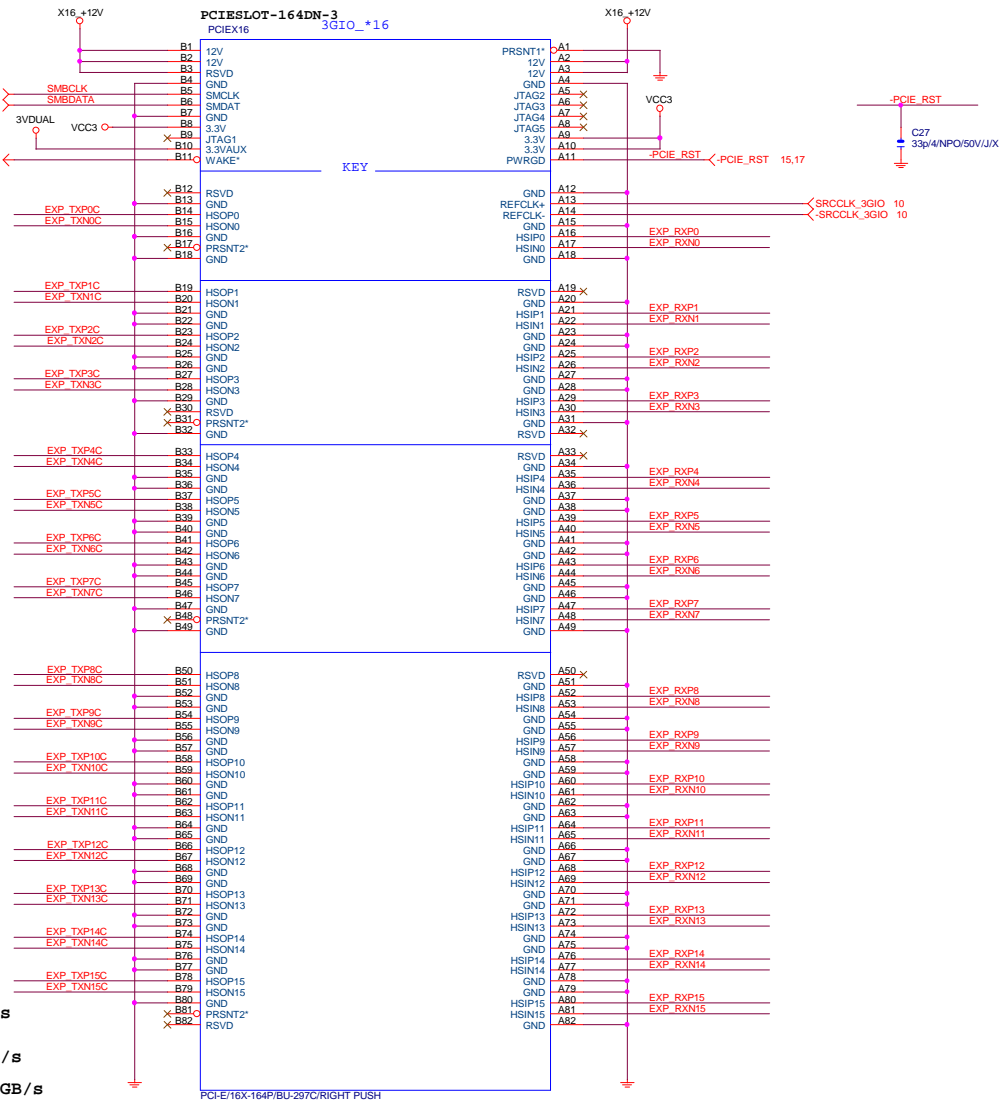
PCE-E X1(單向) BANDWITH=2.5GHZ\*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHZ\*(8b/10b)X2=4Gb/s=500MB/s

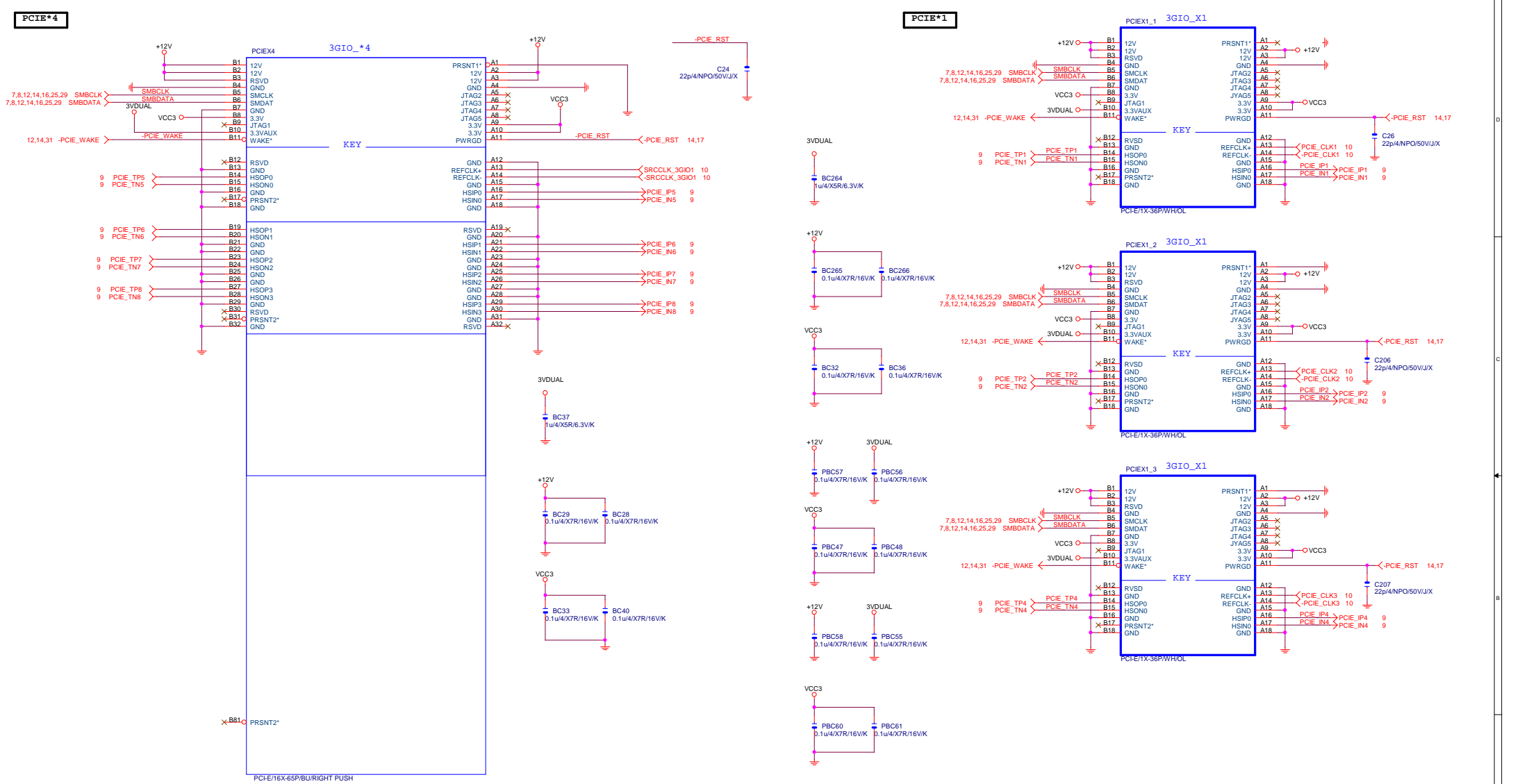
PCE-E X16(單向) BANDWITH=2.5GHZ\*(8b/10b)X16=32Gb/s=4GB/s

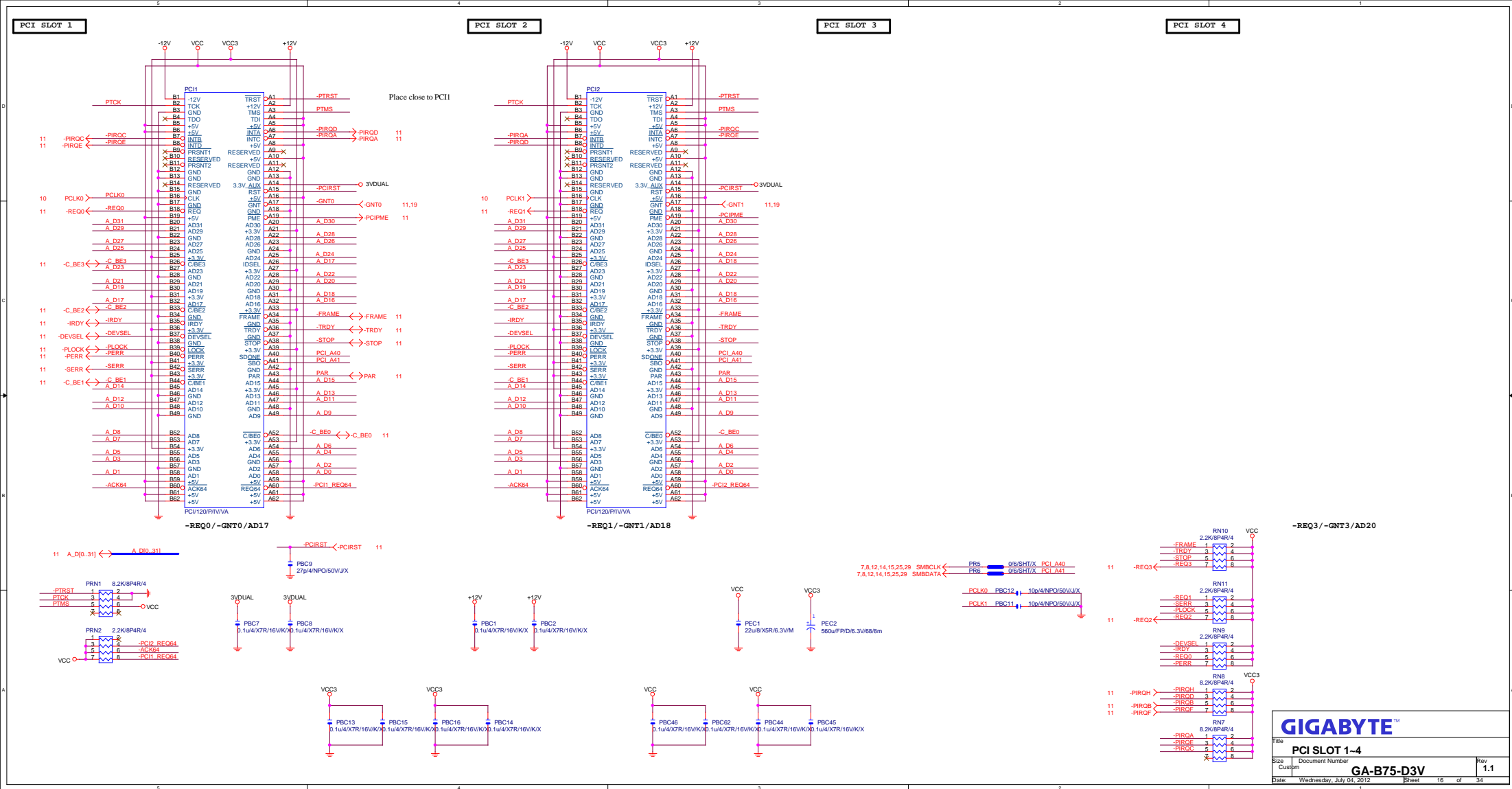
PCE-E X16(雙向) BANDWITH=2.5GHZ\*(8b/10b)X16X2=64Gb/s=8GB/s

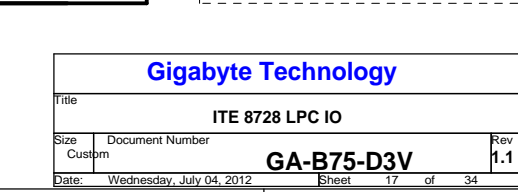
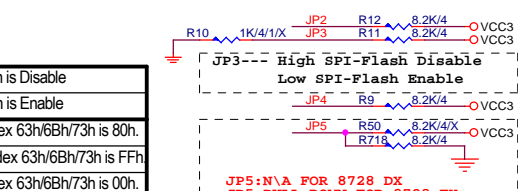
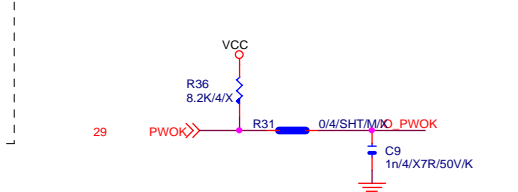
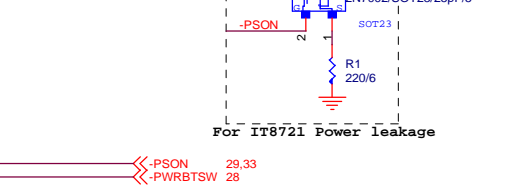
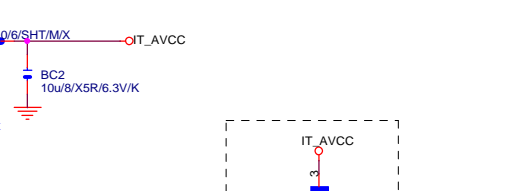
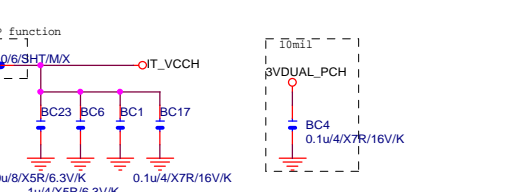
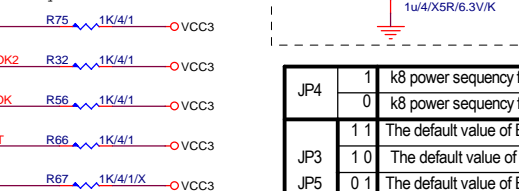
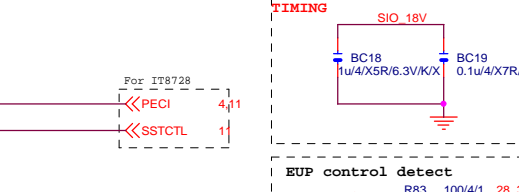
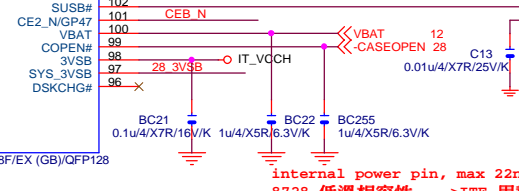
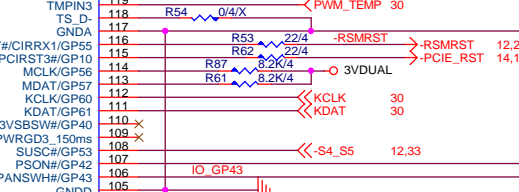
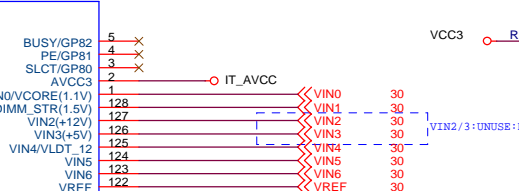
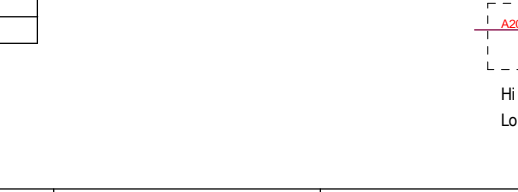
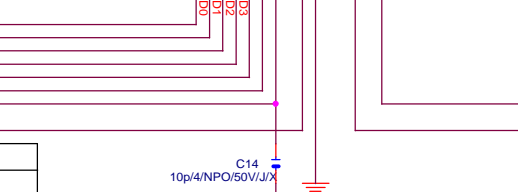
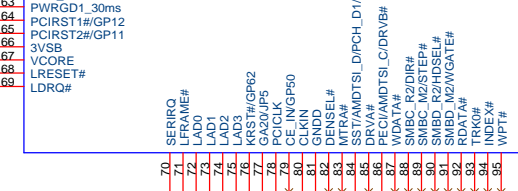
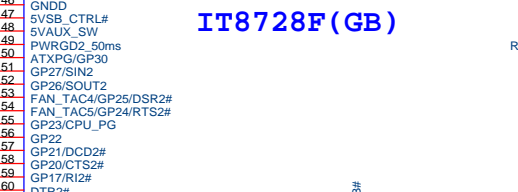
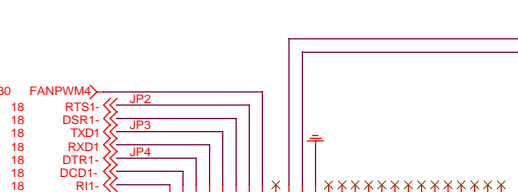
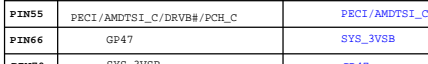
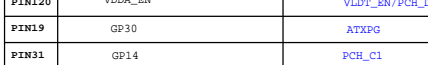
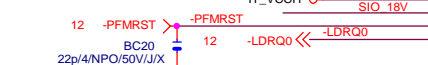
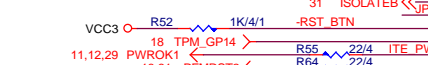
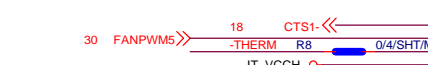
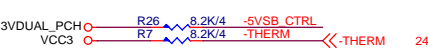
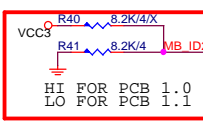
PCI-E REV:2.0--> 5GHZ



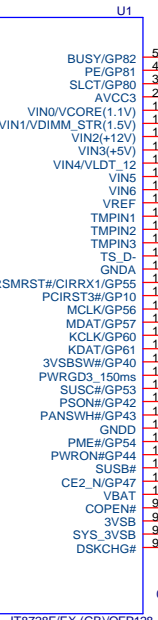




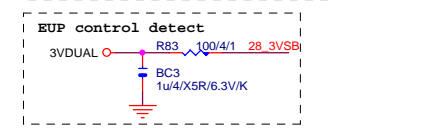
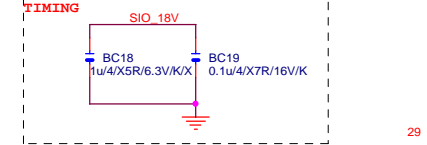




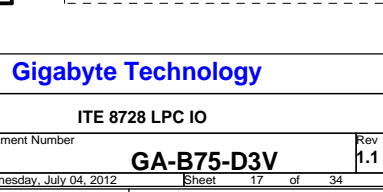
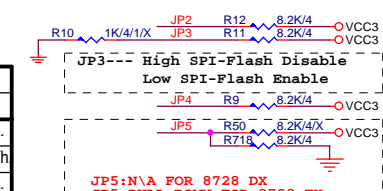
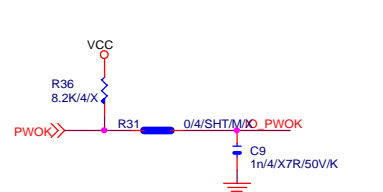
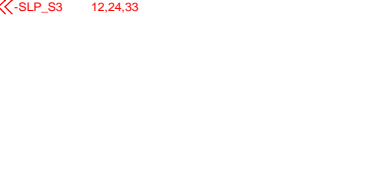
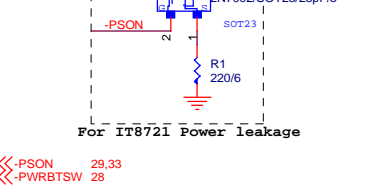
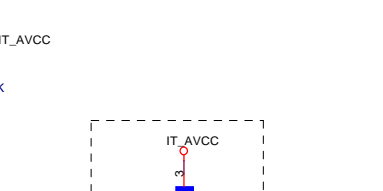
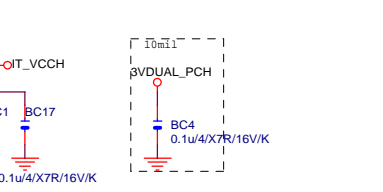
# IT8728F(GB)



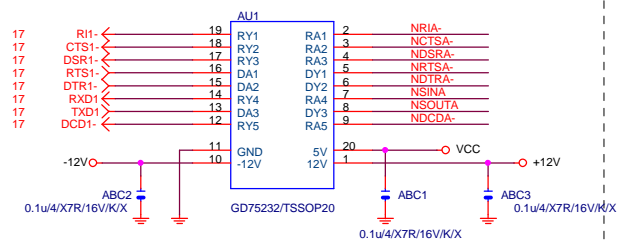
internal power pin, max 22nF cap---->2uf for 8728 低温相容性---->IT8728 用程式卡下不良. FOR EX 改用1颗0.1u 修正VCC0RE / 3VDUAL



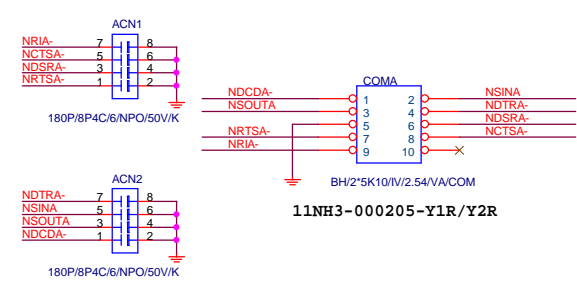
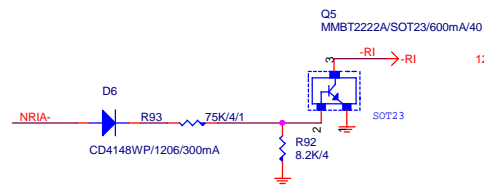
JP4	1	k8 power sequency function is Disable
JP4	0	k8 power sequency function is Enable
JP3	1 1	The default value of EC Index 63h/6Bh/73h is 80h.
JP3	1 0	The default value of EC Index 63h/6Bh/73h is FFh.
JP5	0 1	The default value of EC Index 63h/6Bh/73h is 00h.
JP5	0 0	The default value of EC Index 63h/6Bh/73h is 40h.



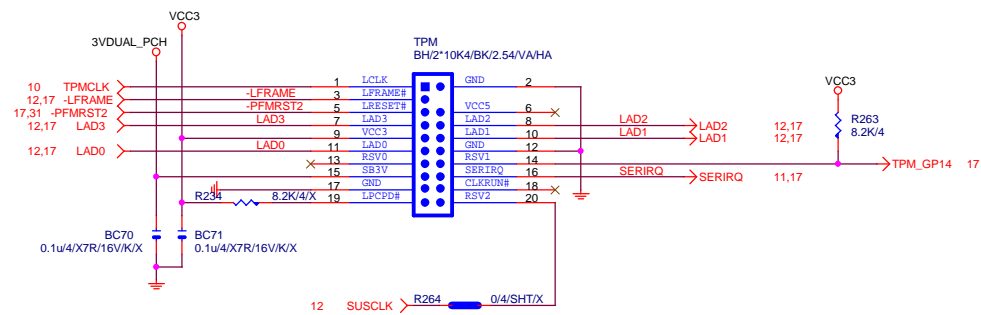
## COMA



## COM RI

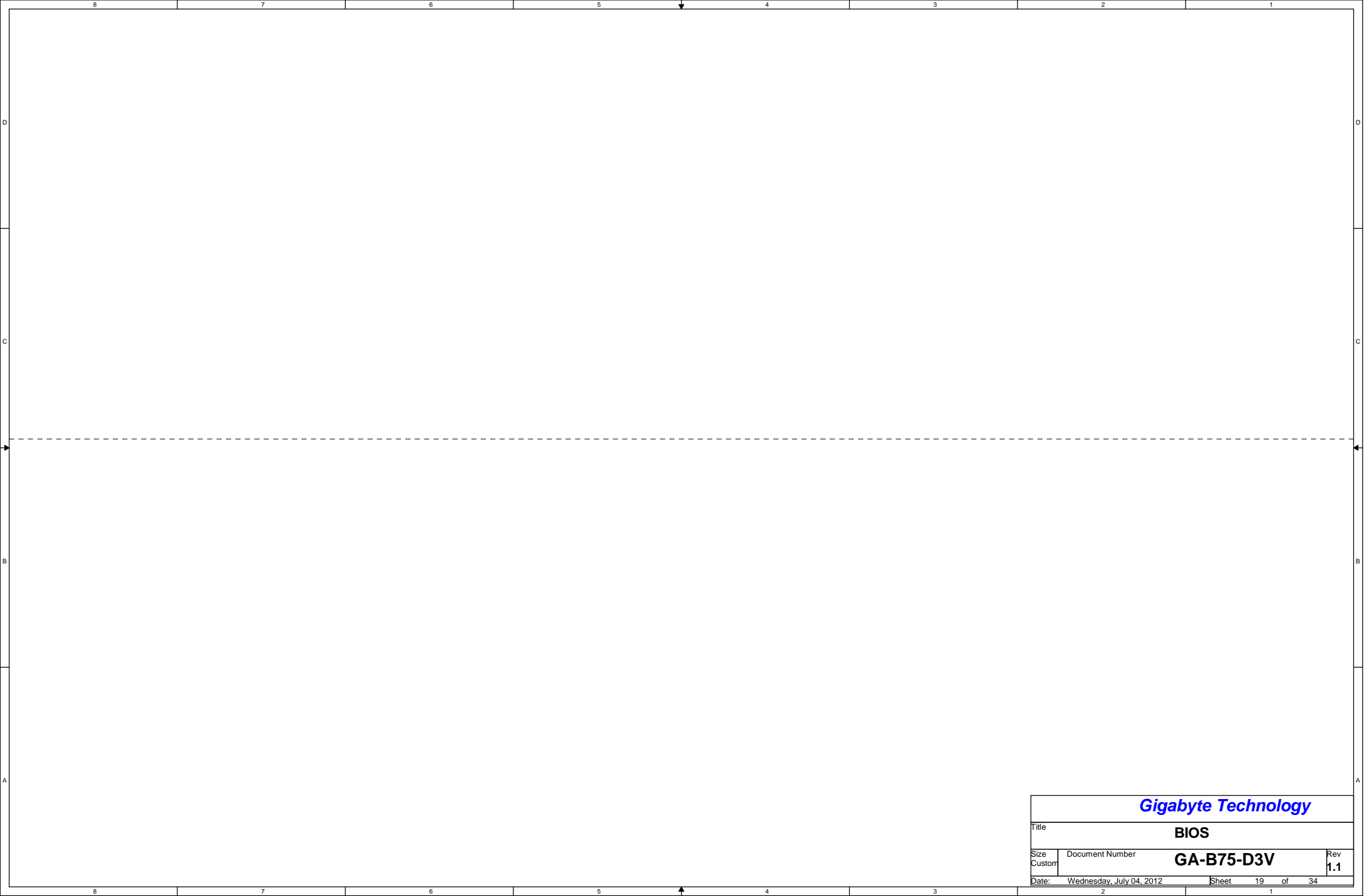


## LPT PORT



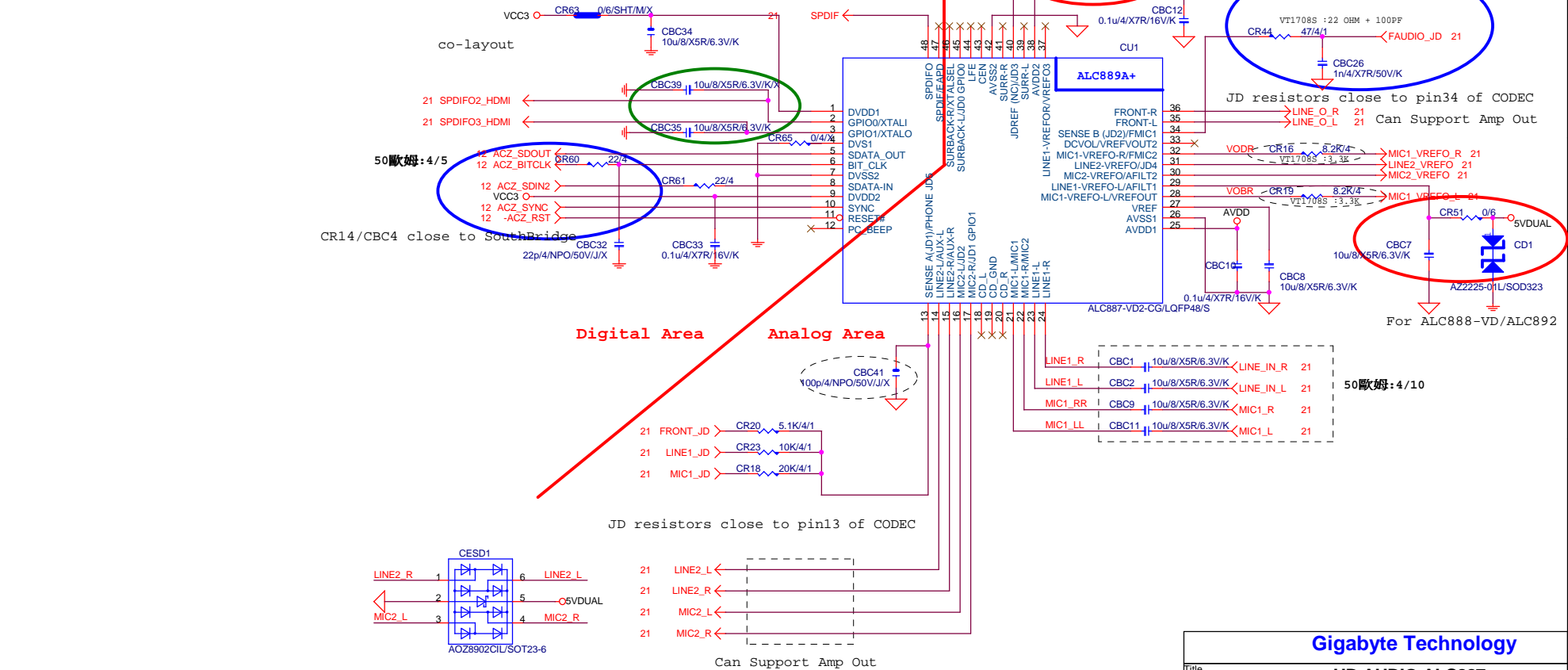
## TPM

Gigabyte Technology			
Title			
COM & Print			
Size	Document Number	Rev	
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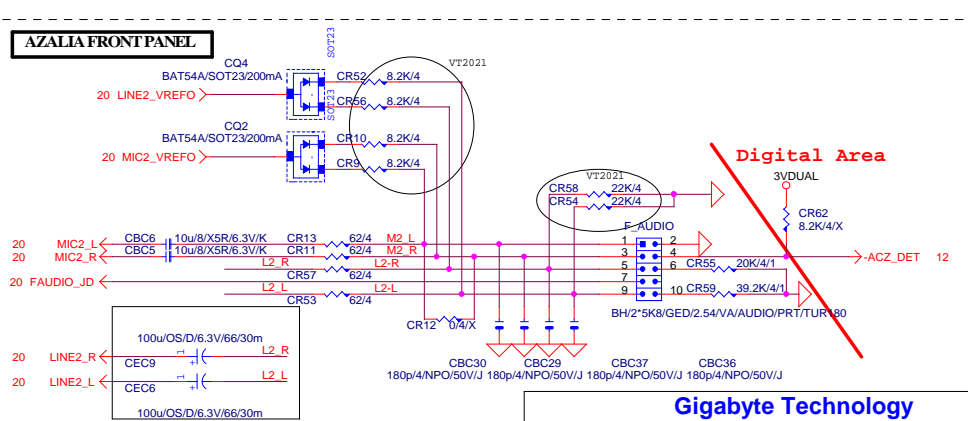
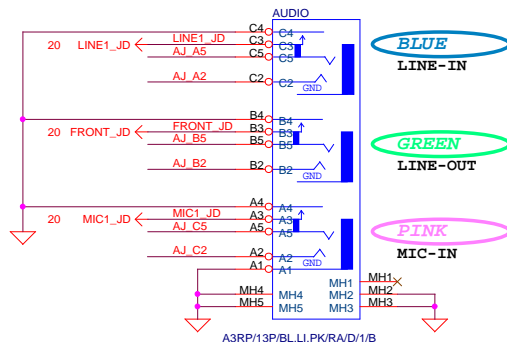
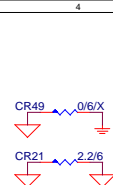


Gigabyte Technology			
Title		BIOS	
Size	Document Number		Rev
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	ALC662	ALC887-VD2	ALC889	VT1708S-CD	VT1708S-CE	VT2021
CR49	X	X	O	O	X	O
CBC36	O	O	X	X	O	X
CR28/CBC11	47ohm+1nF	47ohm+1nF	47ohm+1nF	22ohm+100P	22ohm+100P	47ohm+1nF
CR52	X	O	O	O	O	O
CR57	O	X	X	X	X	X
CBC1/CBC2	10uF/X5R	10uF/X5R	22uF/X5R	10uF/X5R	10uF/X5R	10uF/X5R
CR18	5.11K/4/1	5.11K/4/1	5.11K/4/1	5.1K/4/1	5.1K/4/1	5.1K/4/1
CR36	20K/4/1	20K/4/1	20K/4/1	5.1K/4/1	20K/4/1	5.1K/4/1
CBC38/CBC39	X	X	X	100P/4	100P/4	X
CR10/CR8/CR20/CR45/ CR42/CR51/CR43/CR22/ CR27/CR26	22K/4	22K/4	22K/4	10K/4/1	10K/4/1	10K/4/1
CR7/CR9/CR5/CR13/ CR29/CR32/CR46/CR19/ CR50/CR41/CR21/CR47/ CR2/CR11/CR14/CR24	62 ohm	62 ohm	62 ohm	75 ohm	75 ohm	75 ohm
CFB1/CD1/CBC4	O	O	X	X	O	O
CD2/CD3/CQ3/CQ4	X	X	O	O	X	X







<b>Gigabyte Technology</b>			
Title			
<b>AUDIO JACK</b>			
Size Custom	Document Number	<b>GA-B75-D3V</b>	Rev <b>1.1</b>
Date:	Wednesday, July 04, 2012	Sheet 21 of 34	

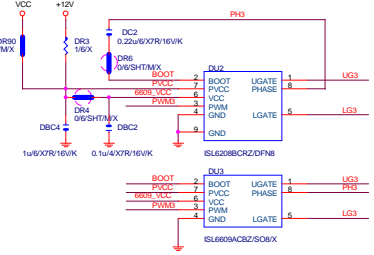




[1]



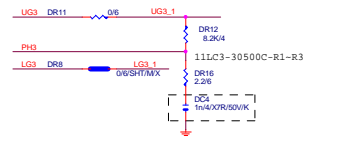
Pop 15L6625CM for P51  
(15L6625CM/008)



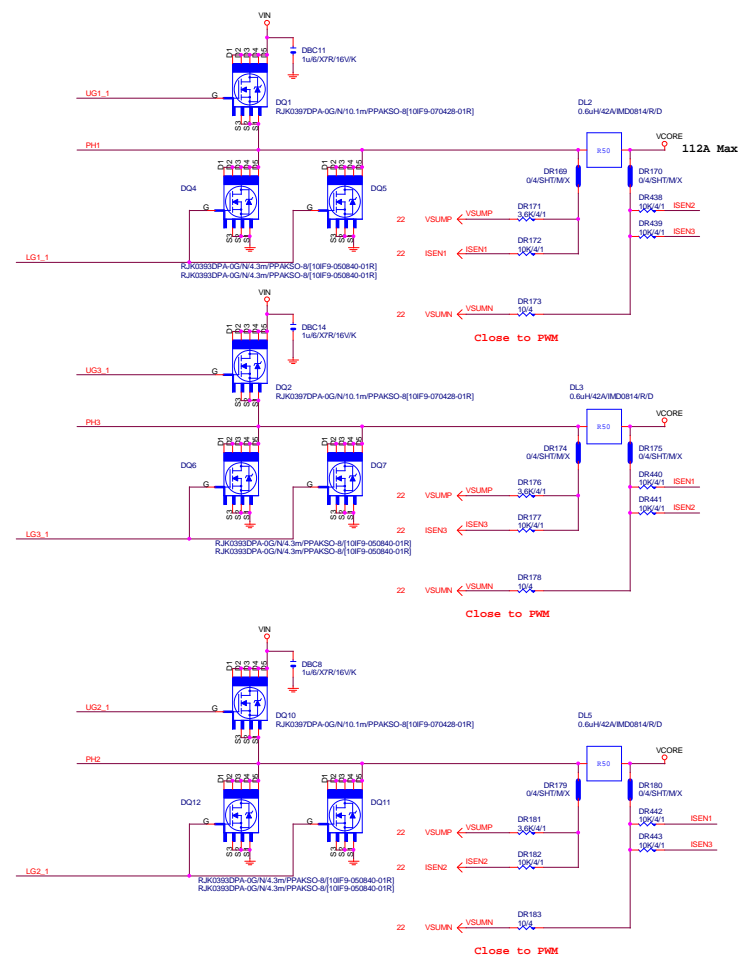
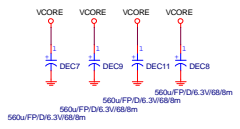
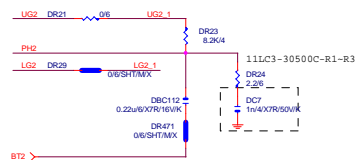
6609 colay with 6208



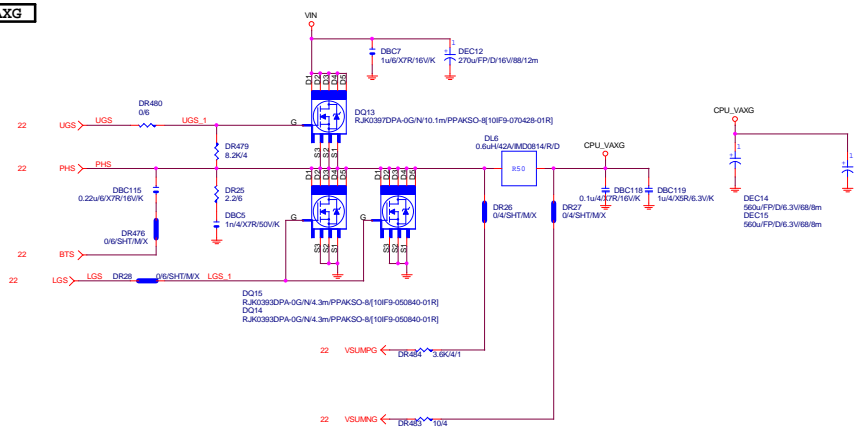
[3]

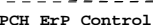
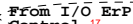
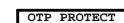
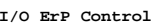
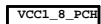


[2]



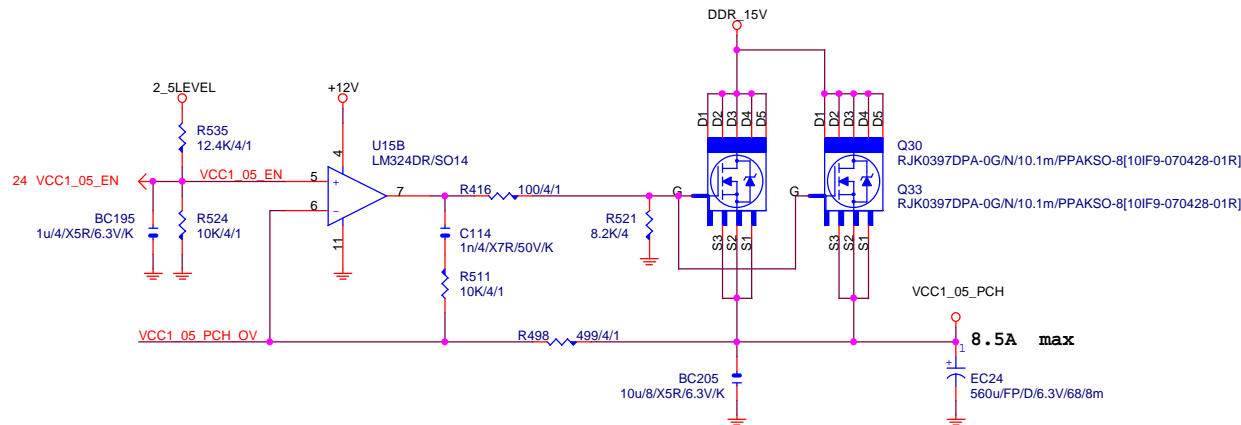
VXAG





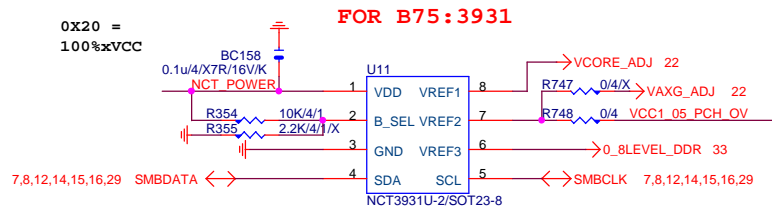
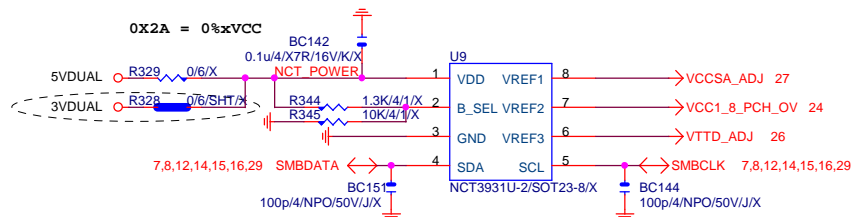
Title			
DISCRETE POWER			
Size C	Document Number	GA-B75-D3V	Rev 1.1
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VCC1\_05\_PCH



Voltage console

ADDRESS	0X2A	0X20	0X22	0X26
R1(K)	OPEN	10	1.3	3
R2(K)	10	OPEN	3.9	2.2
%VCC	0	100	75	42



up6262	0X2A	0X20
VREF1	VCC1_05_PCH	VCORE
VREF2	VCC1_8_PCH	VCCSA
VREF3	CPU_VTT	DDR

Gigabyte Technology

Title

PCH CORE / VOLTAGE CONSOLE

Size B

Document Number

GA-B75-D3V

Rev

1.1

Date:

Wednesday, July 04, 2012

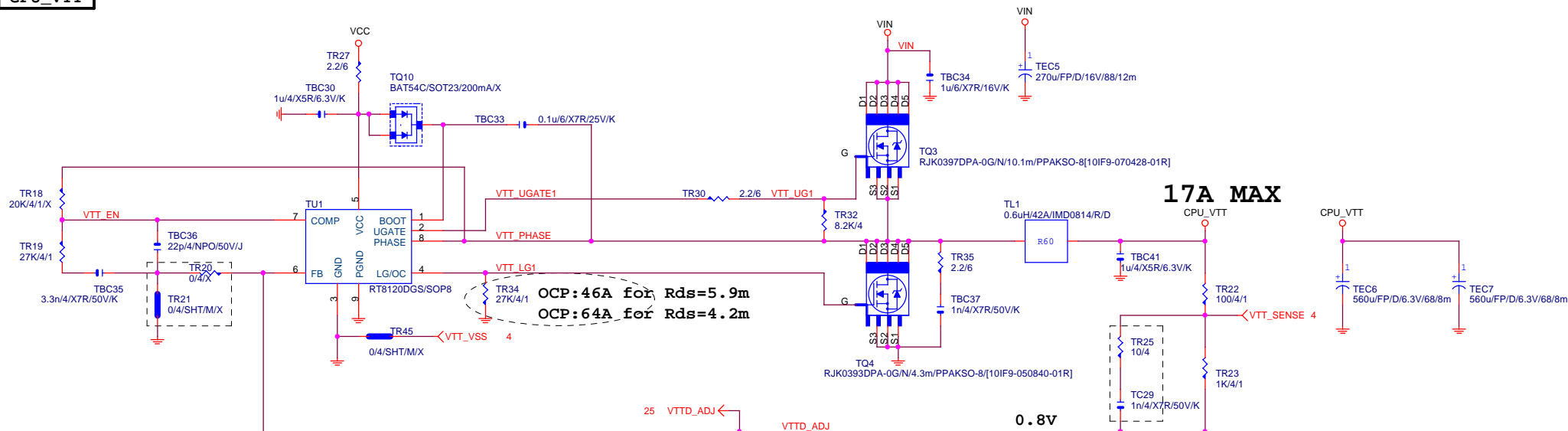
Sheet

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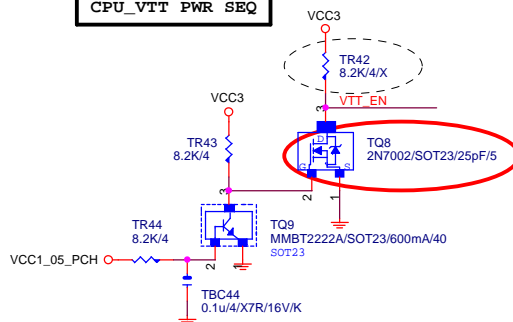
# CPU\_VTT



$$OCP:46A=Ro_{set} \cdot I_{ocset} / R_{ds(on)}$$

$$=27K \cdot 10\mu A / 5.9m$$

## CPU\_VTT PWR SEQ



VTT_SEL	
HI	1.05V
LO	1.0V

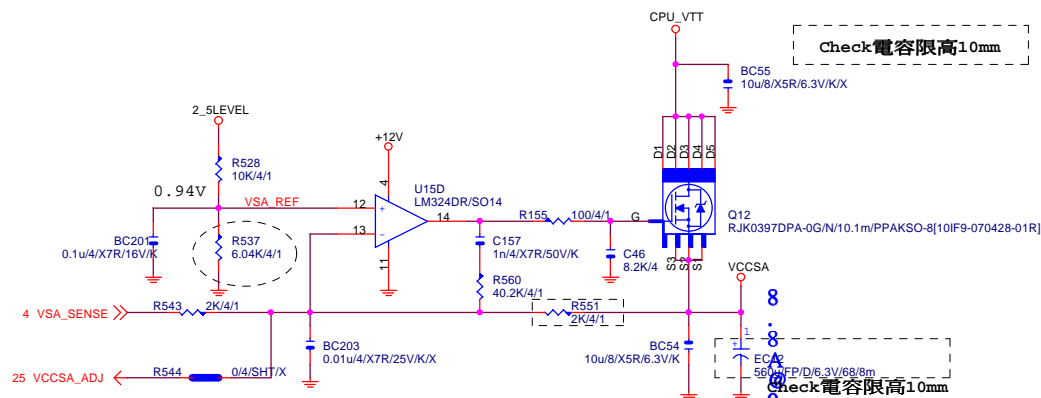
According intel  
CDI/IBP#476733, 固定1.05v

**GIGABYTE™**

Title	RT8120 CPU_VTT		
Size	Document Number	GA-B75-D3V	
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# VCC\_SA



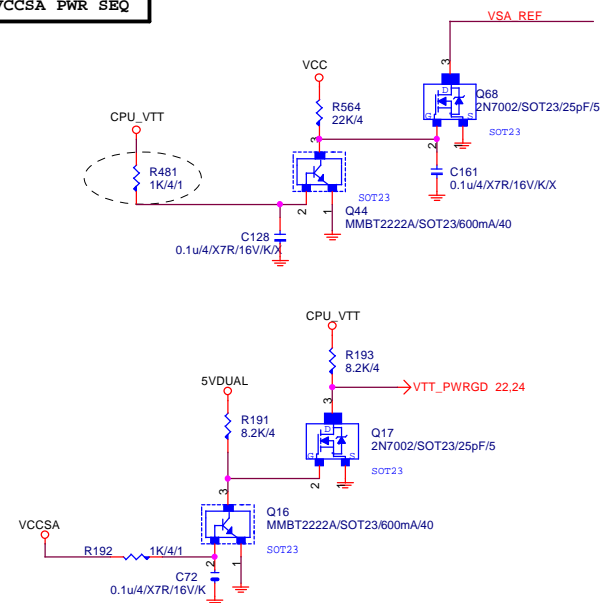
PDG 1.01

VSA_SEL	0.85V
HI	0.925V
LO	0.925V

According intel  
CDI/IBP#476733, 固定0.925V

8  
8  
5  
/ 0  
9  
2  
5  
V

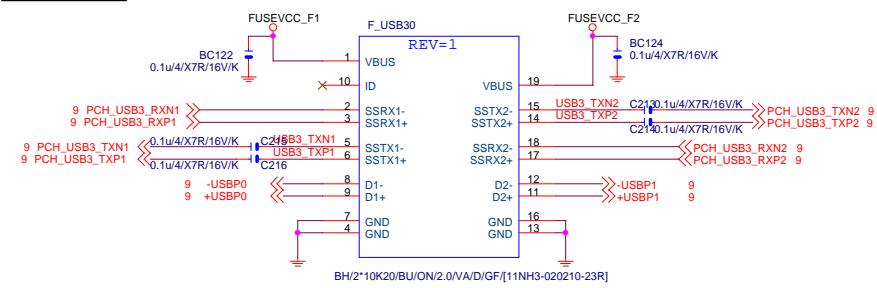
## VCCSA PWR SEQ



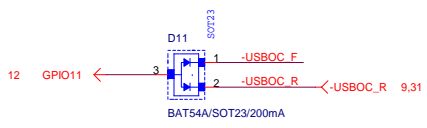
Gigabyte Technology

Title		
VCCSA POWER		
Size	Document Number	Rev
Custom	GA-B75-D3V	1.1
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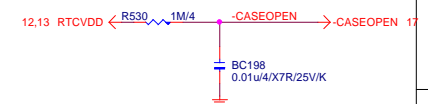
FRONT USB1



F\_USB POWER PROTECT

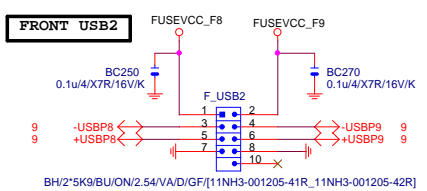
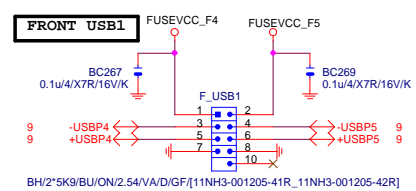


CASE OPEN



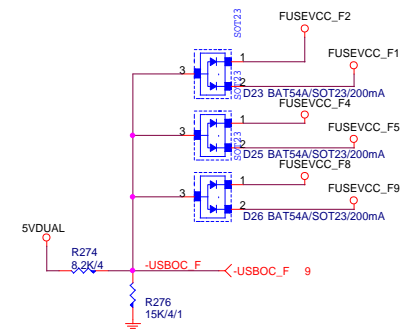
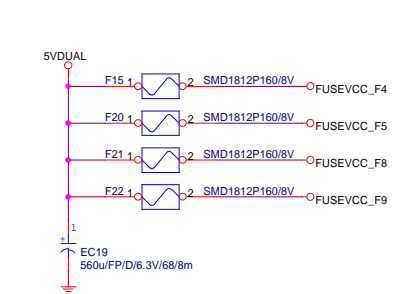
ESD Close to connector

ESD Close to connector

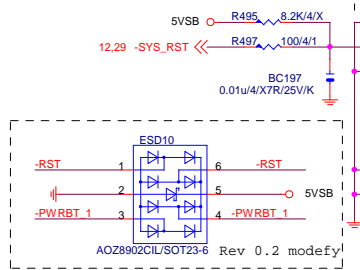


ESD Close to connector

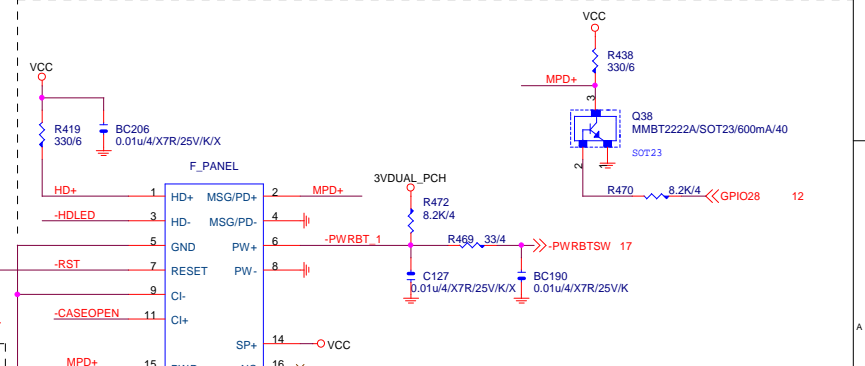
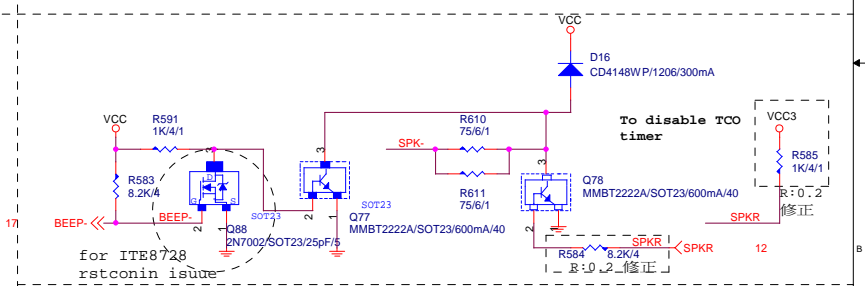
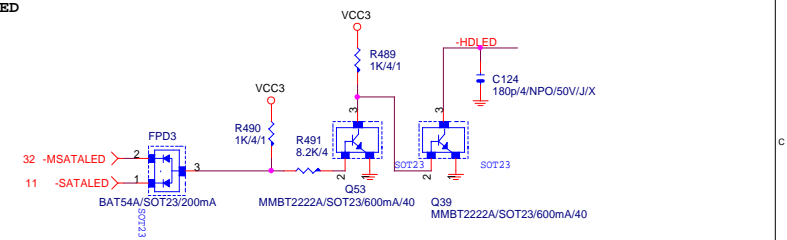
ESD Close to connector



INTEL FRONT PANEL

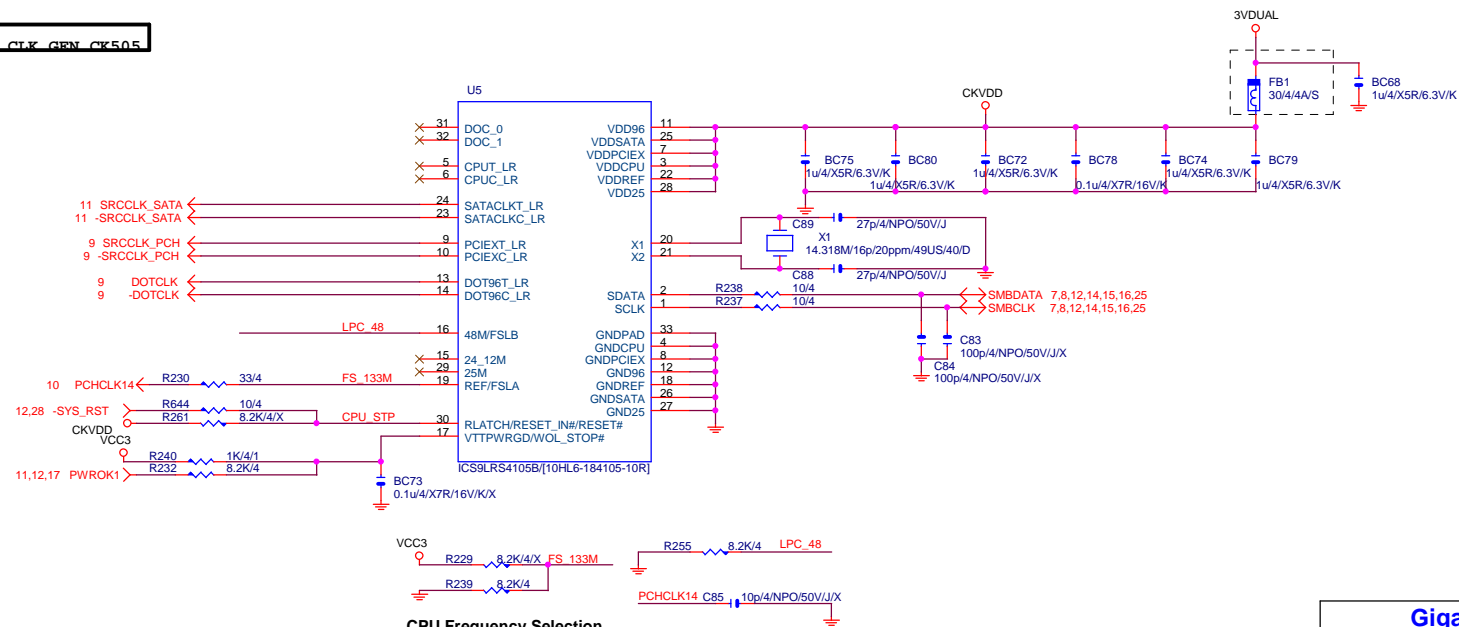
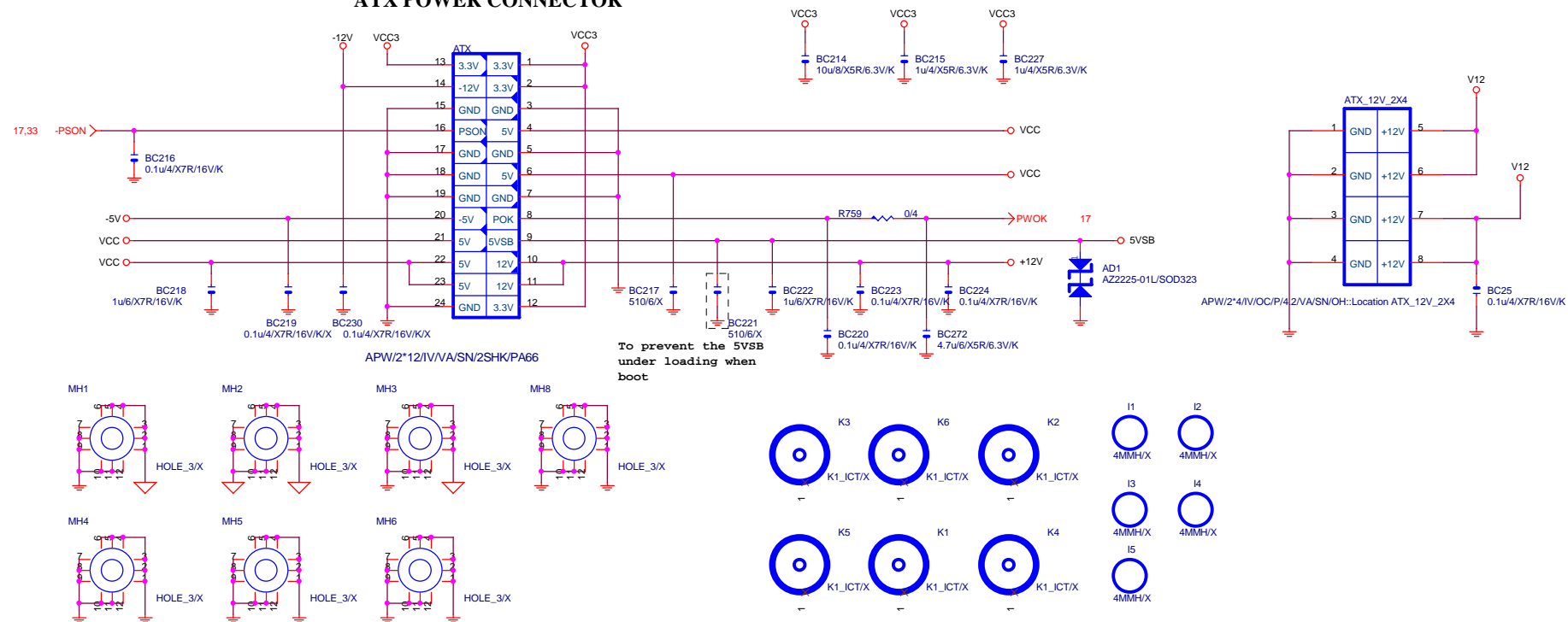


SATA LED



Gigabyte Technology			
Title			
FP,F_USB,USB PWR,FDD,BZ			
Size	Document Number	GA-B75-D3V	
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## ATX POWER CONNECTOR



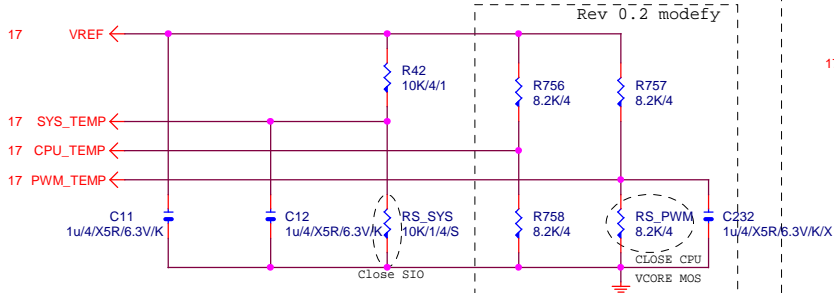
### CPU Frequency Selection

FS	CPU
0	100M <Default>
1	133M

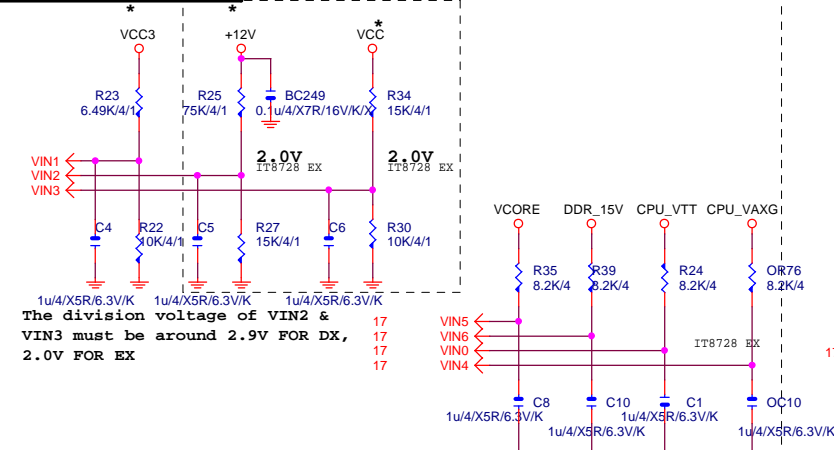
## Gigabyte Technology

Title			
<b>ATX POWER CONNECTOR</b>			
Size Custom	Document Number	<b>GA-B75-D3V</b>	Rev <b>1.1</b>
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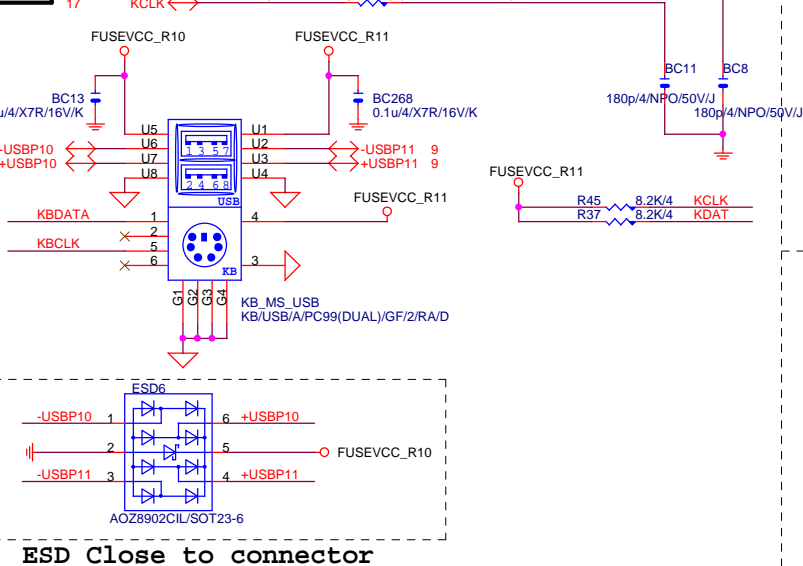
## TEMP H/W MONITOR



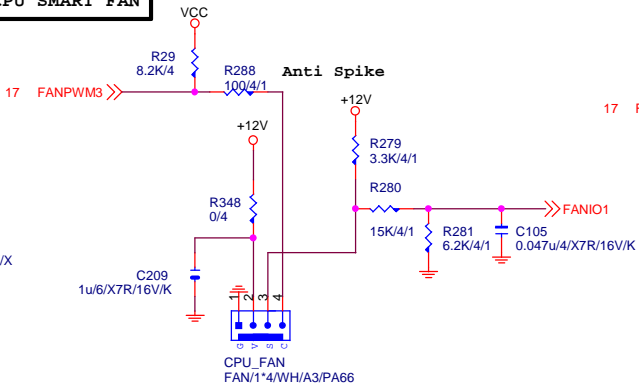
VOLTAGE-- H/W MONITOR



## KB/USB

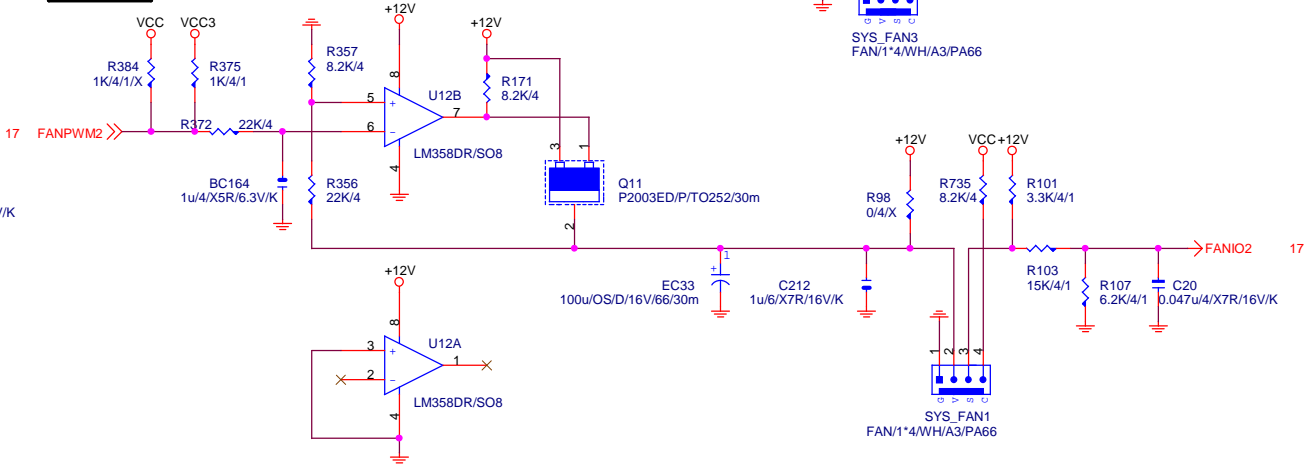


## CPU SMART FAN

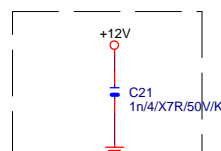
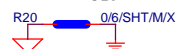


SYS FAN

Linear SYS\_FAN



FOR EMI ONLY



## Gigabyte Technology

Title	HWM.KB/MS. FAN CTRL
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Size	Document Number
Custom	CA

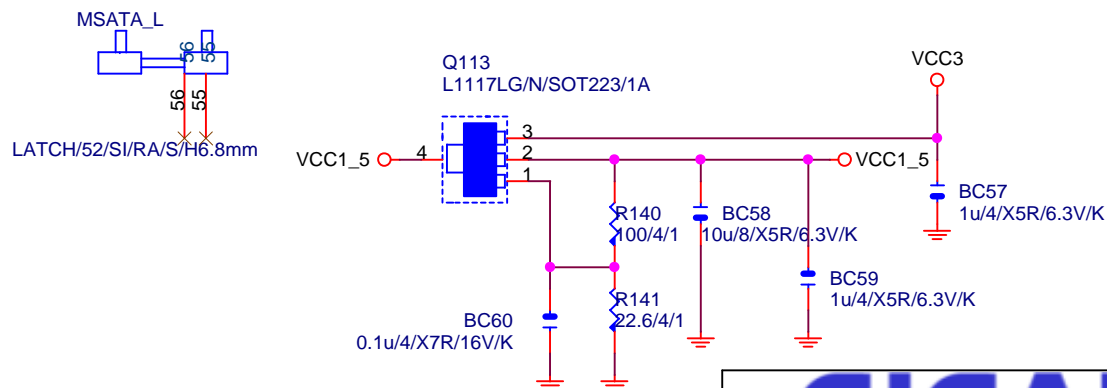
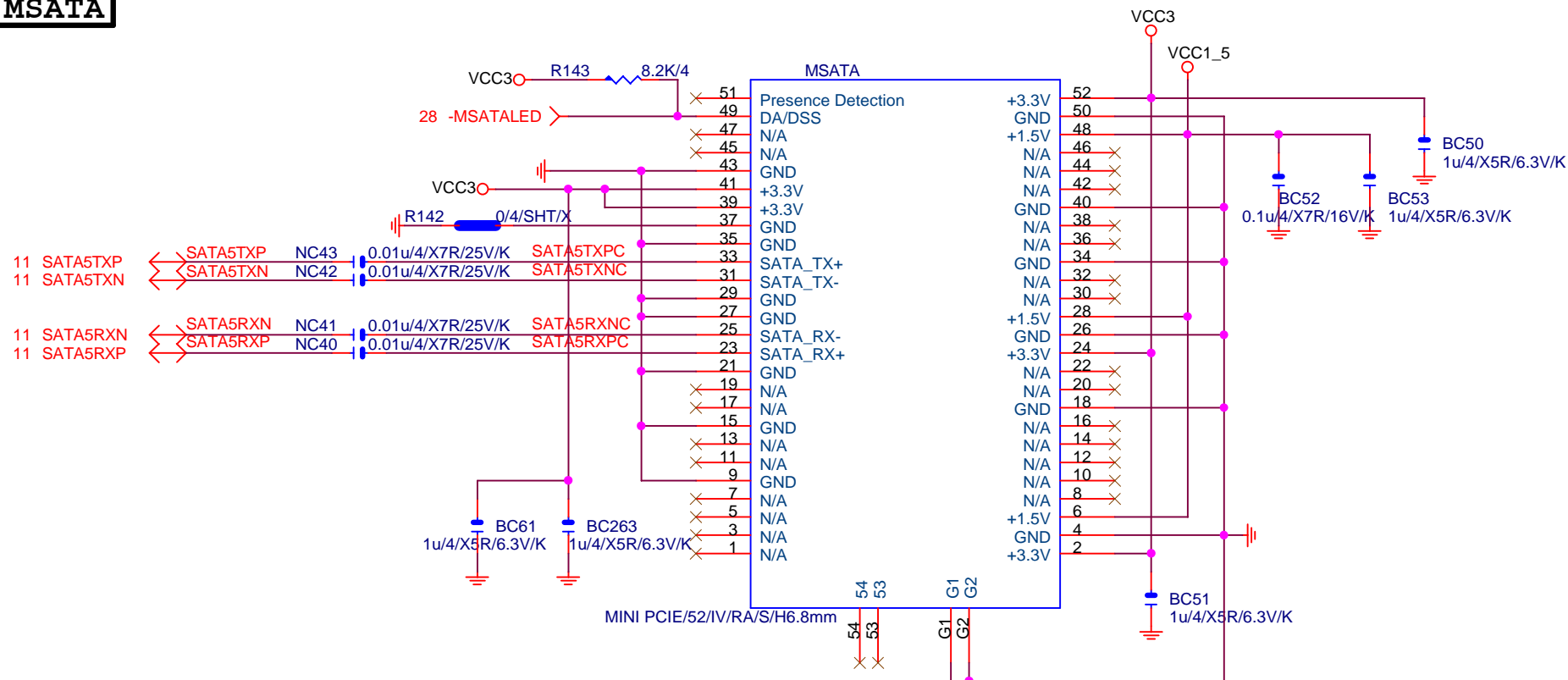
**GA-B7**  
Date: Wednesday, July 04, 2012

Rev
1.1

	1.1
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# MSATA

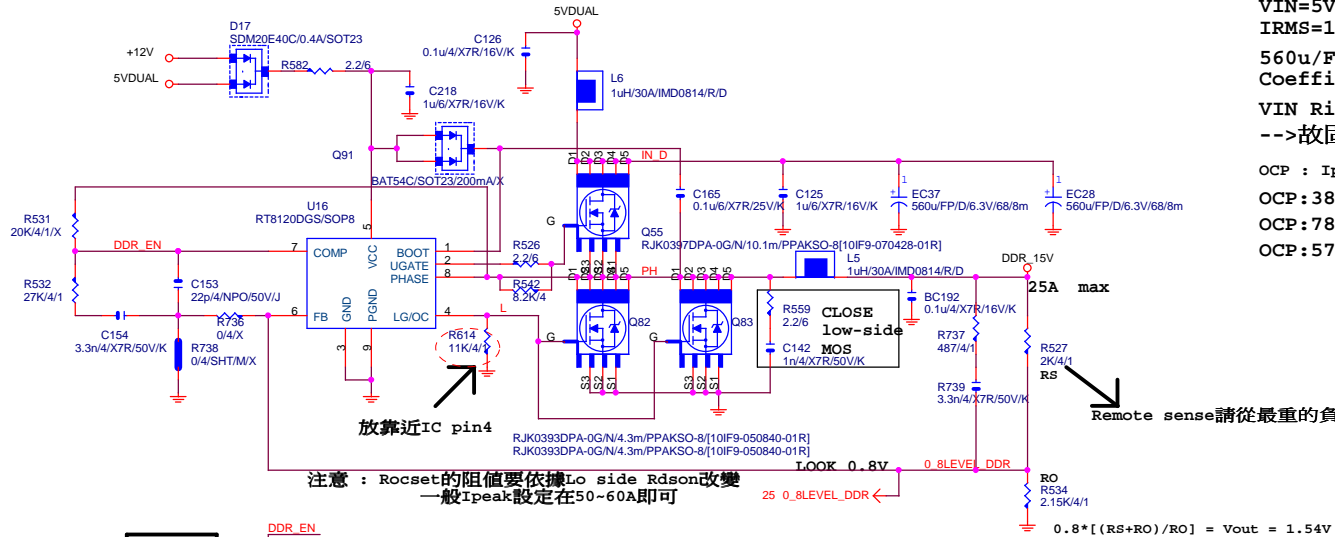


# GIGABYTE™

Title		
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Size	Document Number	Rev
A	GA-B75-D3V	1.1
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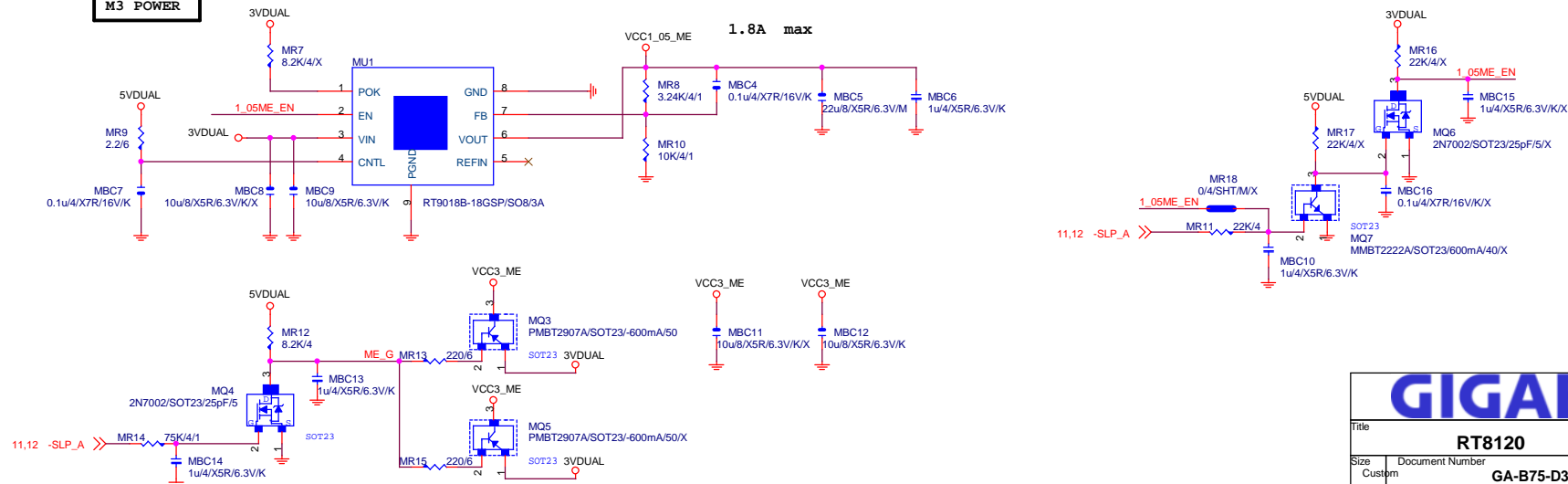
# DDR18V



# PWR SEQ



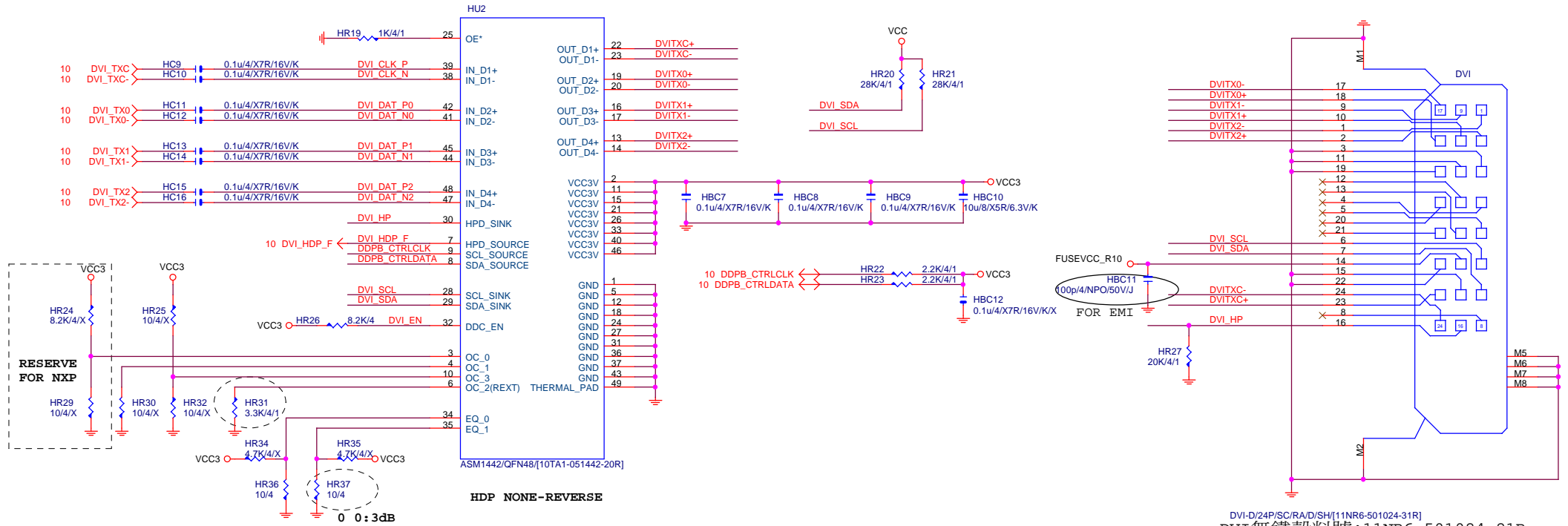
# M3 POWER



**GIGABYTE™**

Title			RT8120
Size	Document Number	Rev	
Custom	GA-B75-D3V	1.1	
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# DVI LEVEL SHIFT



廠商量測結果建議設定 (pin10, pin4, pin3)=(0, 1, 1)

CG2 (PIN10)	CG1 (PIN4)	CG0 (PIN3)	SWING	
0	1	0	450	Default
0	1	1	460	
0	0	1	420	
0	0	0	450	

Gigabyte Technology

Title			DVI
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Custom		Rev	1.1
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